# FOUNDATION OF DIGITAL LIFE



**Investor Relations & Communications**
**November 2026**

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# SILTRONIC AT A GLANCE

**4,300**
employees
worldwide

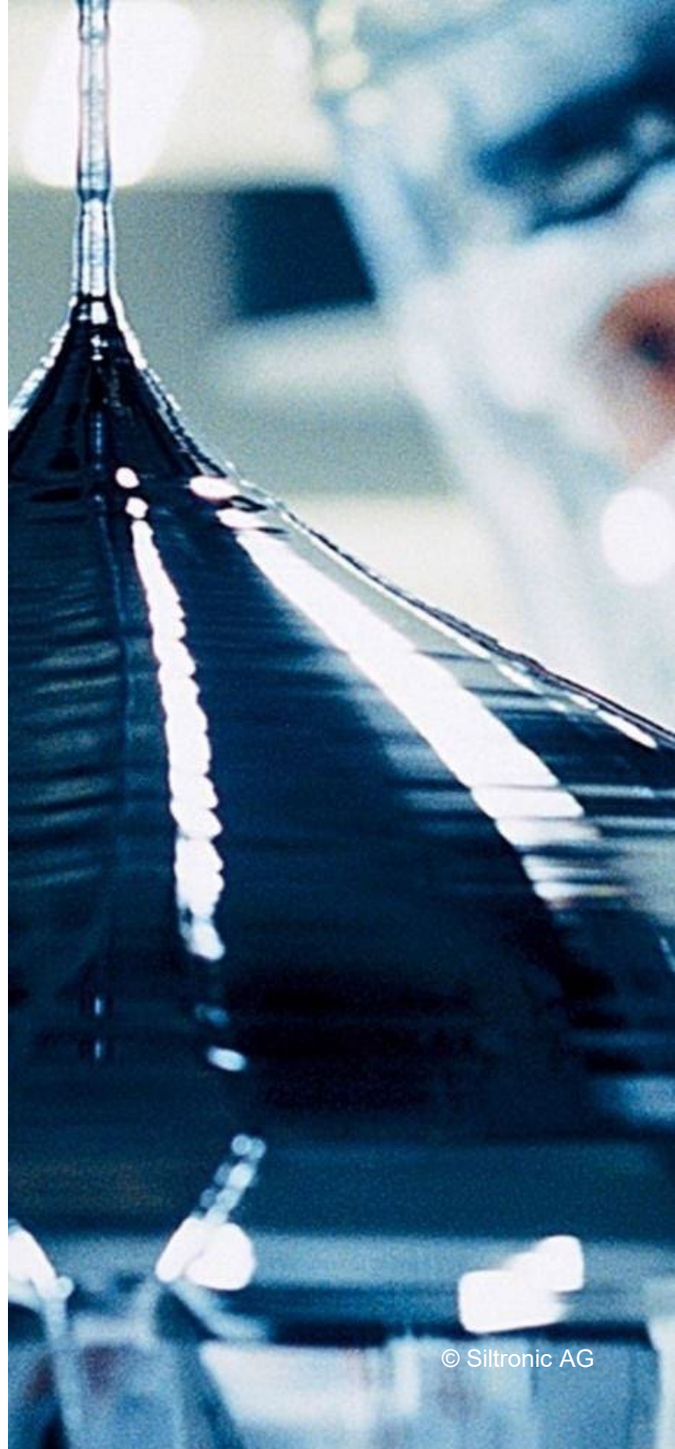
**~60**
years of history
in silicon technologies

**4**
world-class
production sites

**Supplier**
to top
semiconductor producers

**1.4**
billion EUR
sales in 2025

**23.5**
percent
EBITDA margin in 2025




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# ~60 YEARS OF HISTORY – A STORY OF SUCCESS AND INNOVATION

**1968**
Foundation of Wacker-Chemitronic GmbH



**1978**
Foundation of Wacker Siltronic Corporation Portland (US)



**1984**
First 200 mm wafers at Wacker-Chemitronic



**1990**
First research and development projects for 300 mm wafers



**1995**
Acquisition of Freiberg fab



**1997**
Foundation of Wacker Siltronic Singapore Pte. Ltd ("SSP")



**1999**
Start of 200 mm production at Siltronic fab Singapore Pte Ltd.



**2004**
Siltronic opens new 300 mm fab in Freiberg



**2006**
Foundation of 50:50 JV with Samsung ("SSW") and construction start of a 300 mm fab




**2014**
Increase stake in SSW to 78 percent



**2015**
Siltronic executes successful IPO



**2016**
Inauguration of new crystal pulling hall in Freiberg



**2019**
Inauguration of the new crystal pulling hall in Singapore



**2021**
Groundbreaking of new fab in Singapore & expansion of crystal pulling hall in Freiberg



**2023**
Inauguration of crystal pulling hall extension in Freiberg



**2024**
Inauguration of the new fab in Singapore




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# THE WAFER SUPPLIERS ARE FUELING THE WHOLE ELECTRONICS VALUE CHAIN

**Electronics value chain**
(figures for 2025)

**Silicon for electronic applications**
USD 1.4 bn



Wacker Chemie, Hemlock, OCI, Tokuyama

**4**
major suppliers

**Semiconductor silicon wafers**
**USD 11.4 bn**



**5**
major suppliers

siltronic

**Semiconductor device makers**
USD 786 bn



**With fabs**
Samsung, Infineon, TSMC, Intel, Micron, SK Hynix…



**Design only**
Nvidia, AMD, Qualcomm…

**>1,300 fabs**
and roughly **50** larger chip designers

**End markets**
USD >5,000 bn



**Smartphones**
Apple, Samsung…



**Servers / AI**
Microsoft, Google, AWS, Meta…



**PCs**
Lenovo, HP, Dell…



**Automotive**
Toyota, VW, BMW, Tesla…



**Industrials**
Siemens, GE…



**Others e.g.**
White goods, Infrastructure, Gaming…

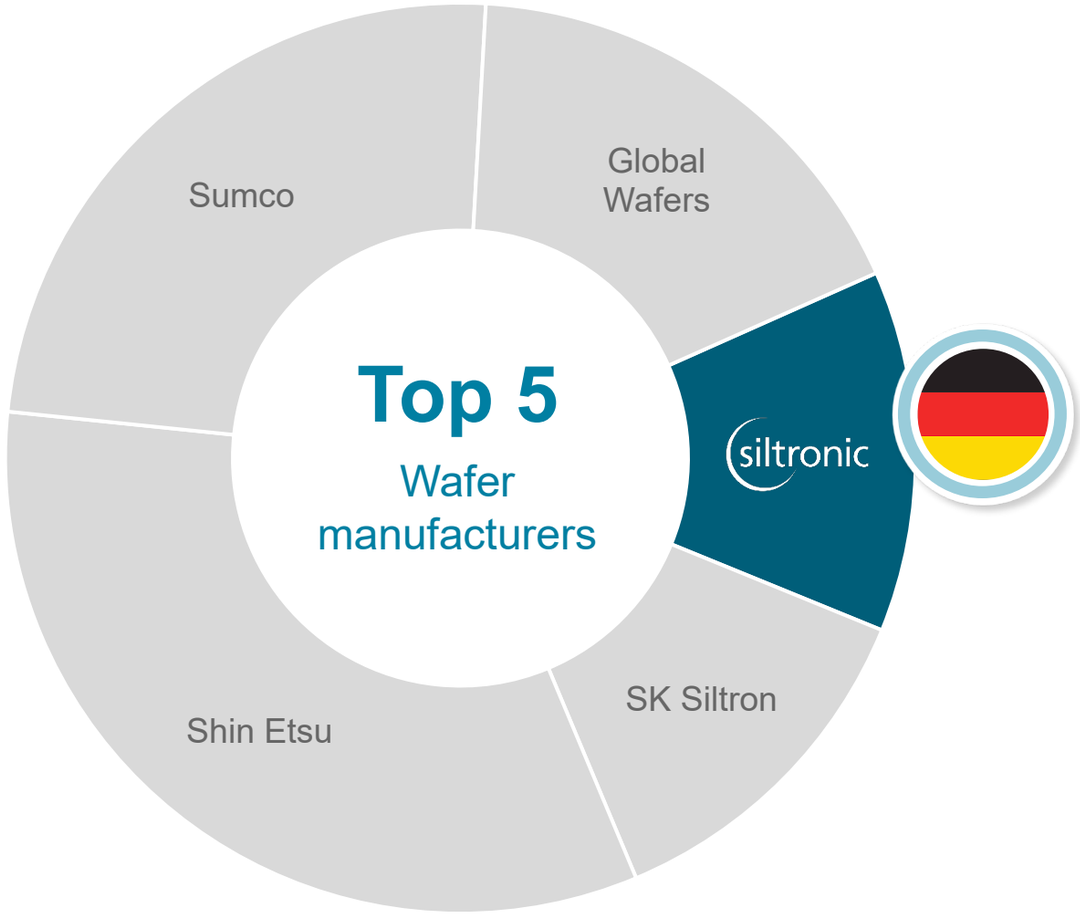
**>10,000**
companies

Source: TechInsights & other companies, WSTS (Silicon based), SEMI SMG, Siltronic Marketing



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# SILTRONIC IS WELL POSITIONED AS THE ONLY WESTERN-BASED WAFER MANUFACTURER




**Around 75%**
of the market is served by the Top 5 wafer manufacturers

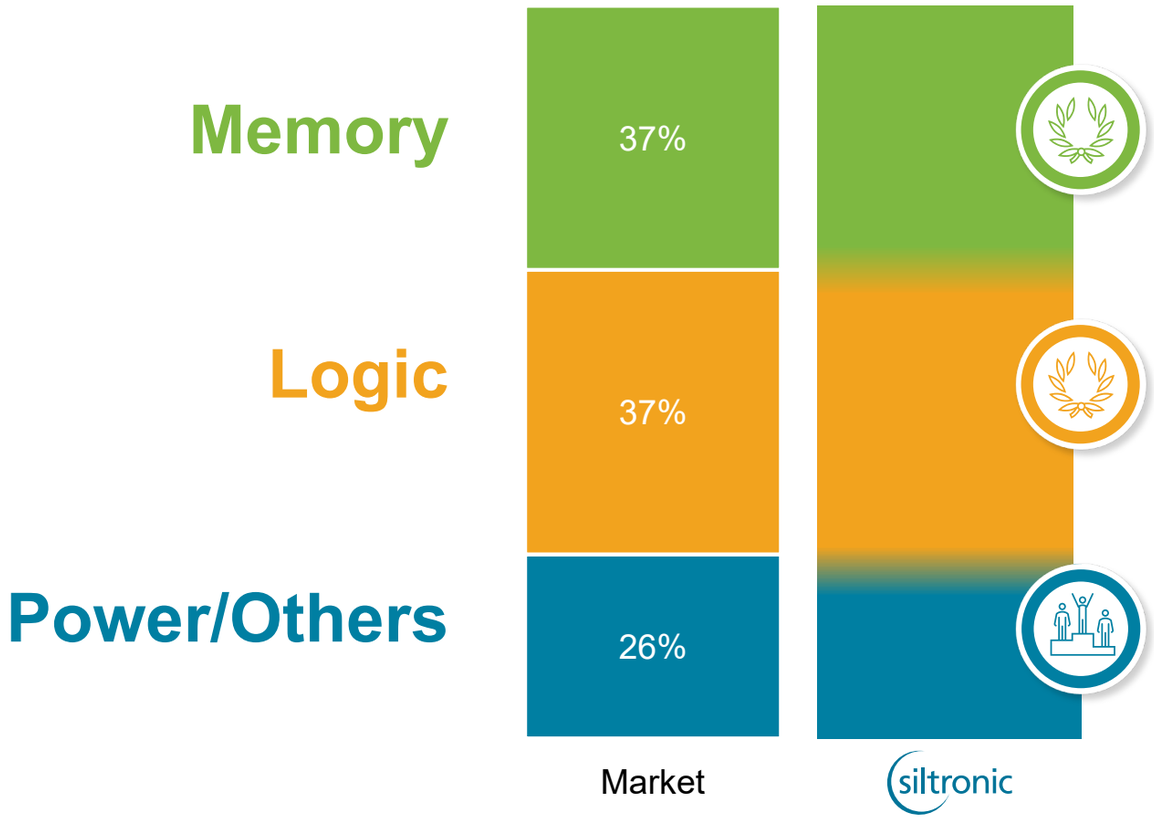
**Only western**
based wafer manufacturer

Source: Q1 – Q3 2025 Quarterly revenue reports by SEMI reporting companies with Siltronic estimates on silicon wafer revenues


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# SILTRONIC HAS A WELL-DIVERSIFIED AND RESILIENT PRODUCT MIX IN ALL THREE SEMI-SEGMENTS

**Demand by segments** in % of total demand in 2025




**Leading Edge**
Supplier

**Power**
Leading position

Source: Siltronic Marketing


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# A GROWING INDUSTRY DRIVEN BY MEGATRENDS

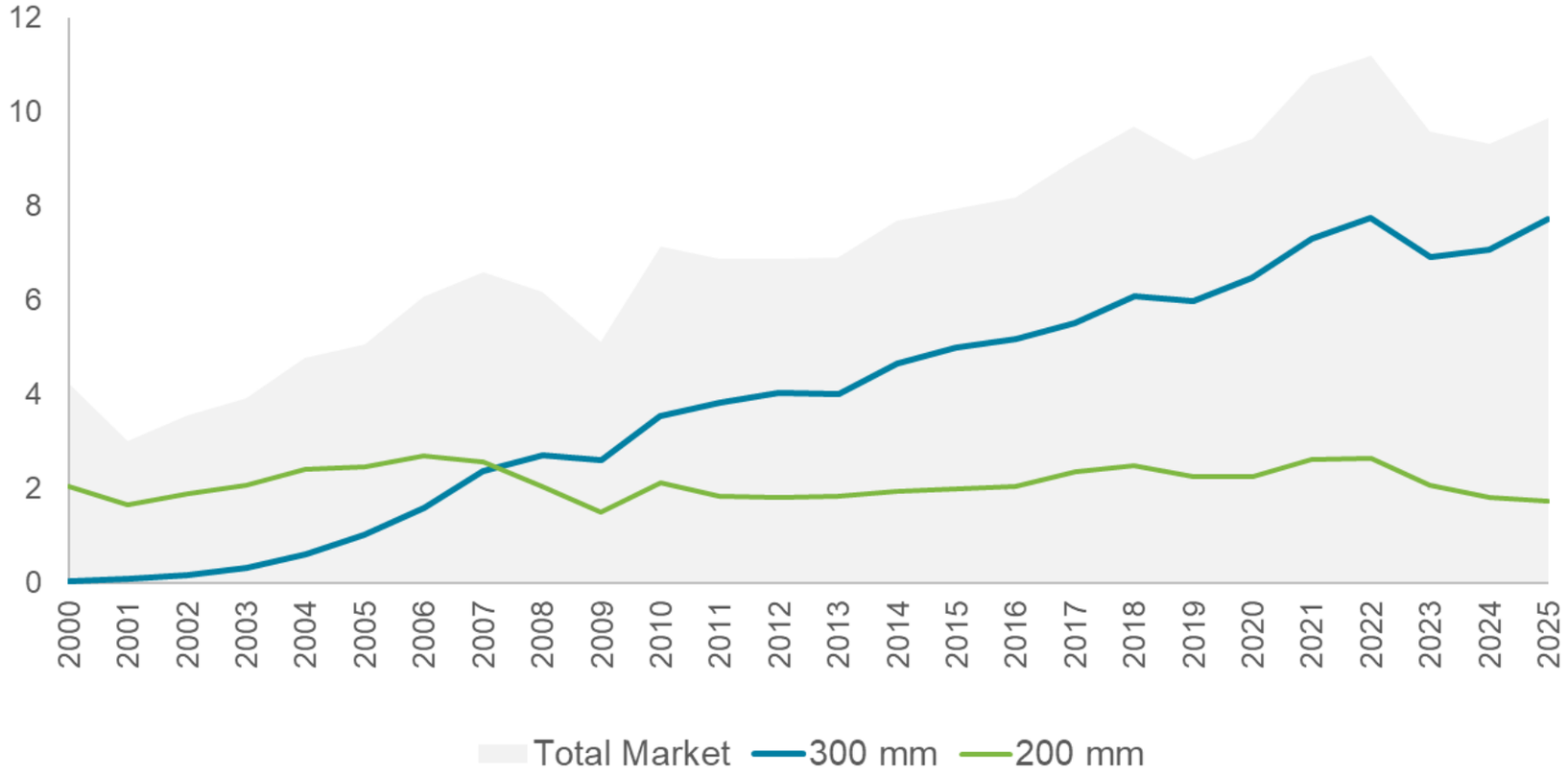




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# WAFER DEMAND GROWTH EXPECTED TO CONTINUE WITH A CAGR OF 5 TO 6 PERCENT

## Wafer Demand per Diameter
in mn 300 mm equivalents




**CAGR 6%**
for 300 mm

**CAGR 1%**
for 200 mm

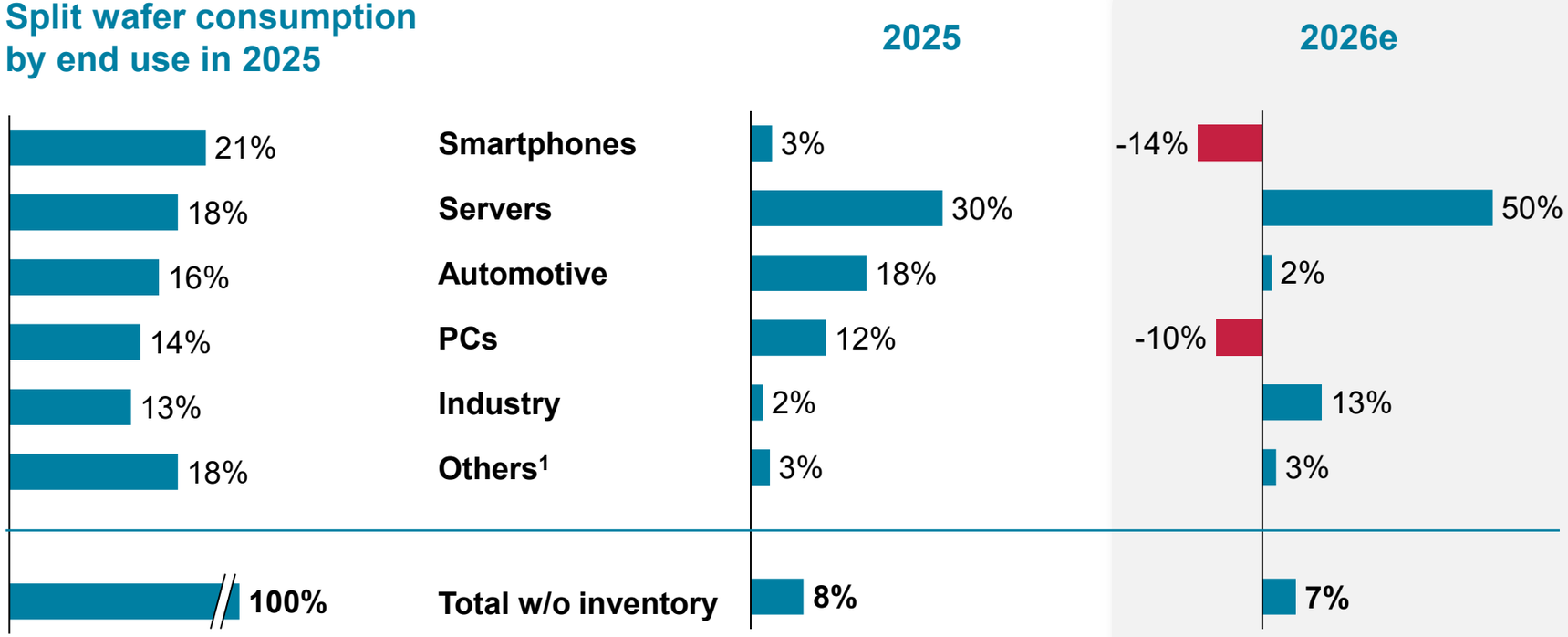
Source: Siltronic estimates excluding China; [1]CAGR = Compound Annual Growth Rate (2000-2025)


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# END MARKETS (PRE INVENTORY) EXPECTED TO GROW BY 7 PERCENT

## Growth in Wafer Area Consumption, in % yoy

### Split wafer consumption by end use in 2025




**AI**
driving servers and industry

**Shortage**
in memory chips weighing on smartphones and PCs

**Inventory**
restocking in memory contributing to wafer demand

[1]Infrastructure Telecommunications, game consoles, consumer electronics such as televisions, white goods such as refrigerators
Source: Siltronic estimates as of September 2026


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# MEGATRENDS CONTINUE TO DRIVE WAFER DEMAND

Wafer demand

Artificial Intelligence

Digitalization & Connectivity




Renewable energy & Sustainability

Electromobility & Autonomous driving

## Siltronic
is well positioned to support each megatrend

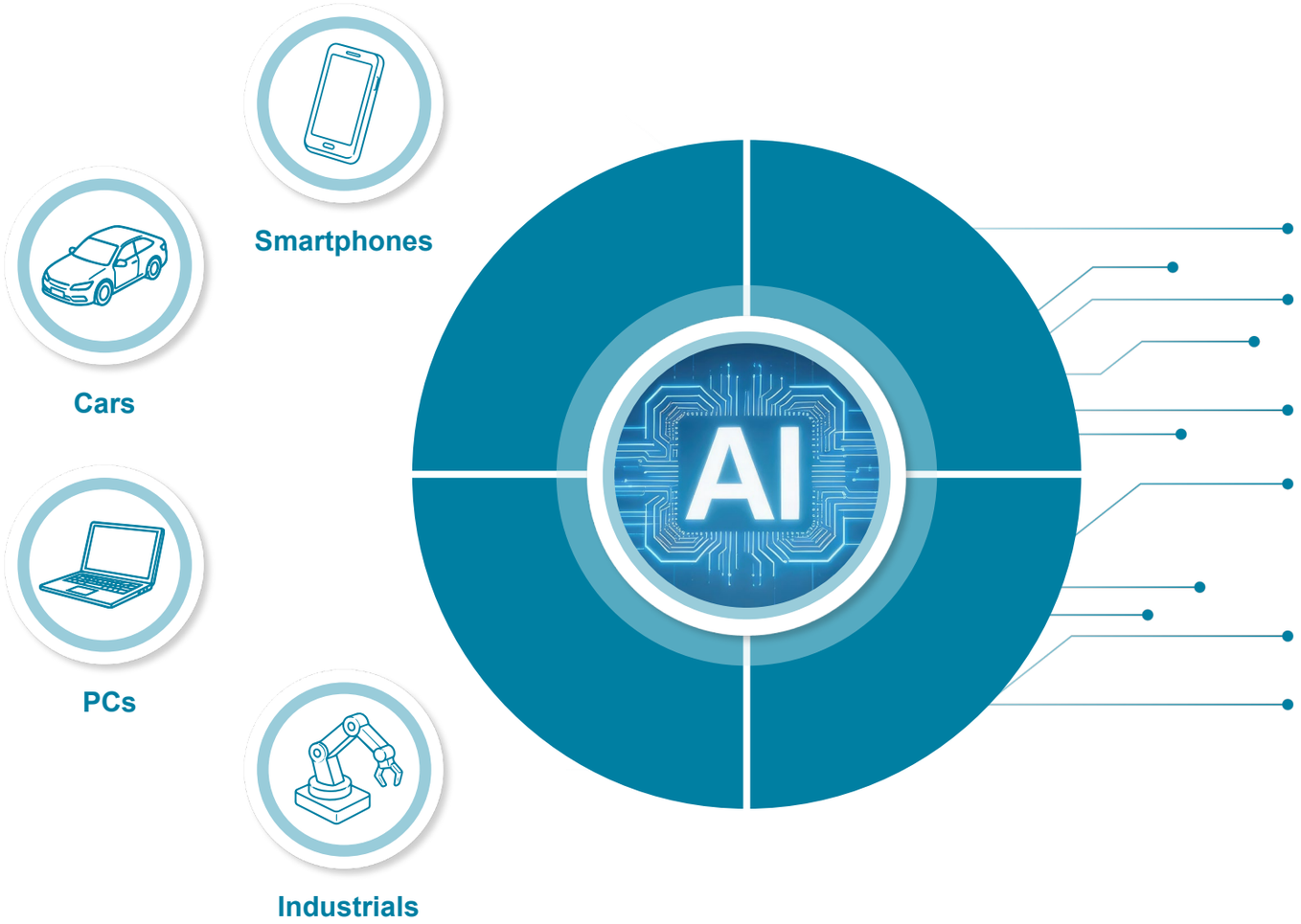
## All segments
are growing

## Key benefits
from strategic focus



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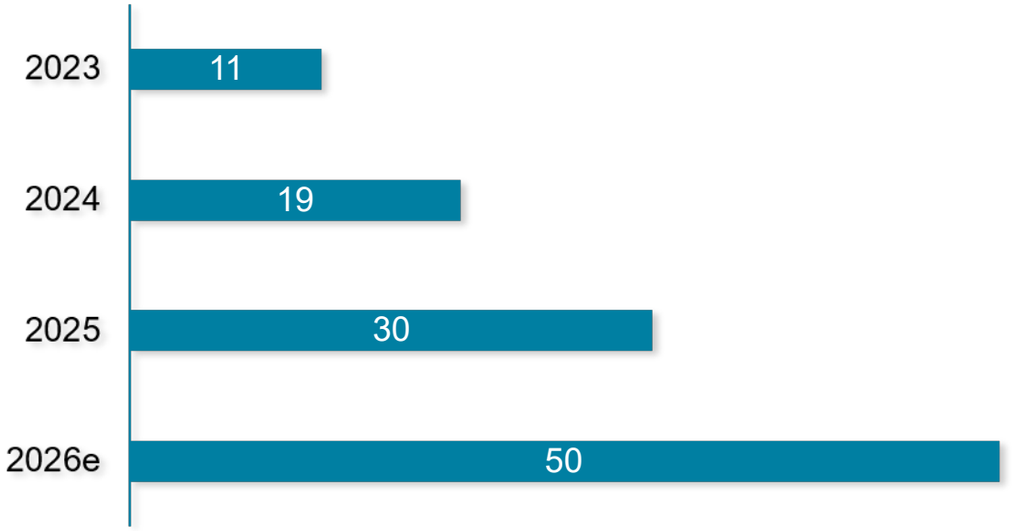
# AI IS DRIVING WAFER DEMAND ACROSS ALL END MARKETS, WITH STRONGEST IMPACT ON SERVERS






## Servers

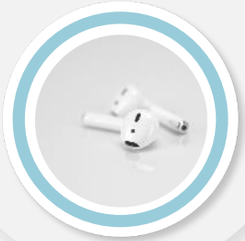
Growth in wafer area consumption (% yoy)





© Siltronic AG

# AND CONTENT IN END PRODUCTS IS GROWING WITH NEW PRODUCTS ENTERING THE MARKET

| | Smartphone (high end) | Car (upper middle class) | Server | Smartwatch | Wireless earphones |
|---|---|---|---|---|---|
| |  |  |  |  |  |
| **Content 2015[1]** | 9 $cm^2$ | 54 $cm^2$ | 230 $cm^2$ | | |
| **Content 2025[1]** | 12 $cm^2$ | 110–130 $cm^2$ | 600–770 $cm^2$ | 2–4 $cm^2$ | 1–2 $cm^2$ incl. base station |
| | More Memory and Image sensors | Digitalization, ADAS and Entertainment | Cloud and Artificial Intelligence | Examples for new applications that did not exist ten years ago | |

Source: Siltronic estimates, [1] Silicon wafer area per device



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# GLOBAL FOOTPRINT AS A TECHNOLOGY LEADER





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# OUR INTERNATIONAL PRODUCTION NETWORK SERVES CUSTOMERS AROUND THE GLOBE

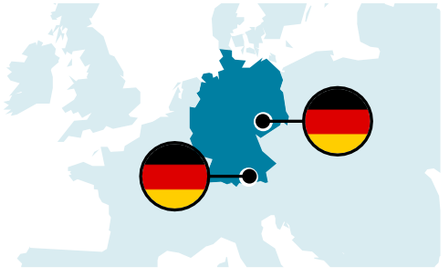
## Singapore



Singapore

SSW* CP 300 mm SSP 200 mm

## Germany



Germany (Burghausen, Freiberg)

CP 300 mm

## USA



USA (Portland / Oregon)

200 mm

**300 mm**
volume production in Germany & Singapore

**R&D**
hub in Burghausen

**Leading Edge**
production

*JV with Samsung Electronics

JV = Joint Venture
CP = Crystal Pulling
R&D = Research and Development



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# ADDITIONAL SINGAPORE CAPACITY SUPPORTS DEMAND

Fab **operating as planned** and serving Leading Edge customers

## FAB HIGHLIGHTS

**EBITDA margin** at group level since Q3 2026

Targeted mid-term **EBITDA margin: 50%+**

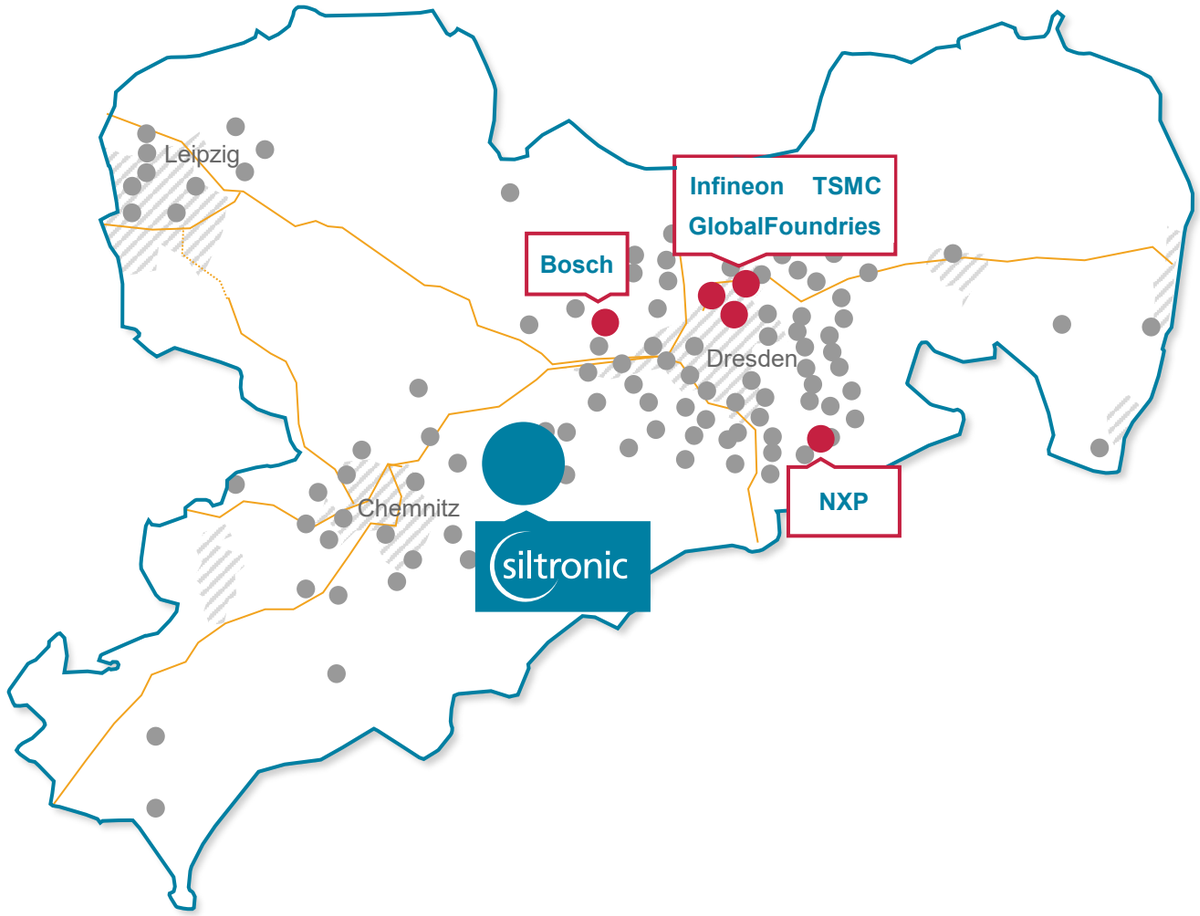
Highly **automated** and substantial **epi share**, with high **economies of scale**

**Further ramp** depending on market development



# IN FREIBERG WE FURTHER IMPROVED PRODUCT MIX FUELED BY INVESTMENTS






**Every 3rd**
semiconductor in the EU is from this region

**Investments**
of more than EUR 1 bn since 1995 by Siltronic

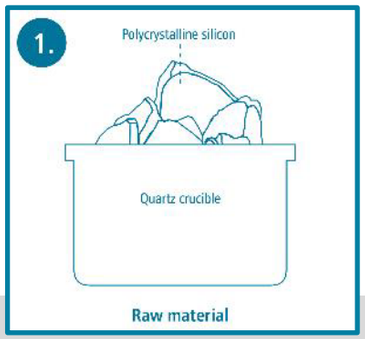
**First-class**
geographical position in light of EU ambitions

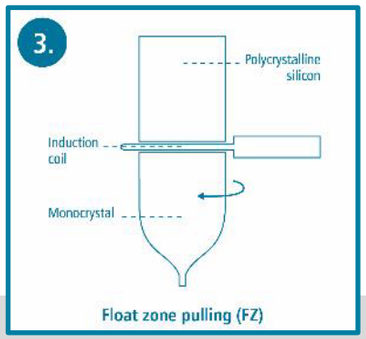
Source: Siltronic; Silicon Saxony/Wirtschaftsförderung Sachsen

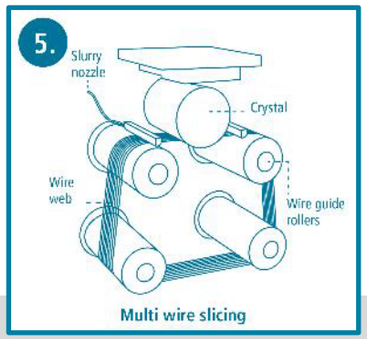


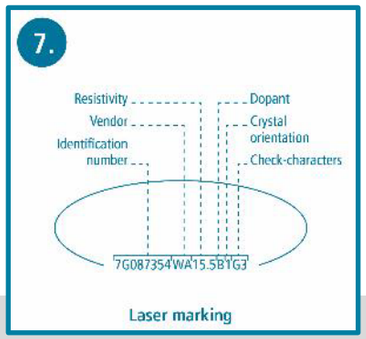
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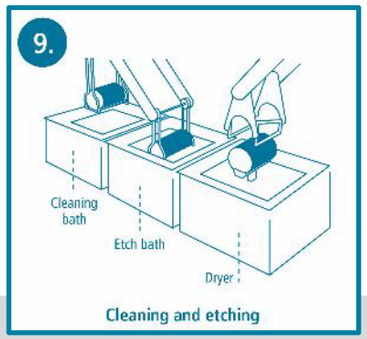
# SIMPLIFIED PRODUCTION PROCESS: MORE THAN 60 STEPS REQUIRED FOR A PERFECT WAFER

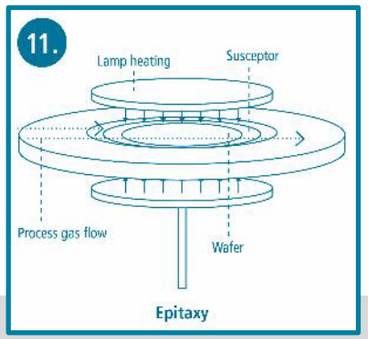


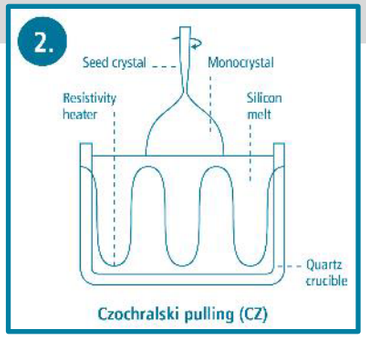



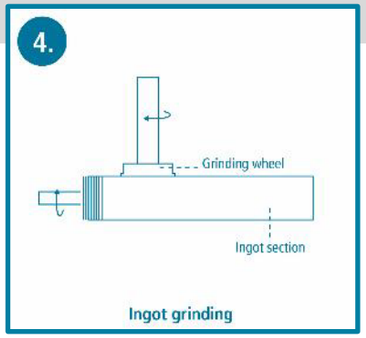



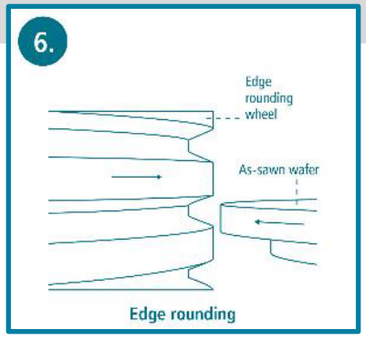



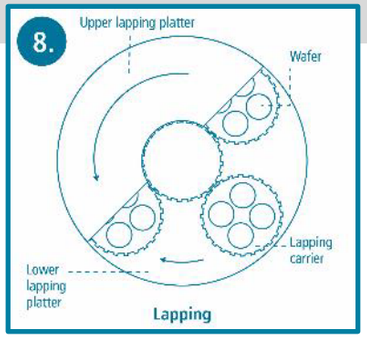



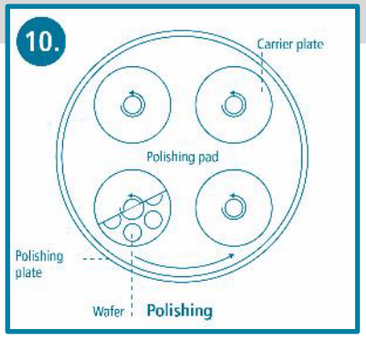





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# STRONG FOCUS ON GROWTH DRIVERS
# R&D AND INNOVATION

**R&D**
hub in Burghausen

**450**
R&D employees

**1,900**
patents

**6%**
of sales spent on R&D in 2025

**Partnering with** leading innovation platforms

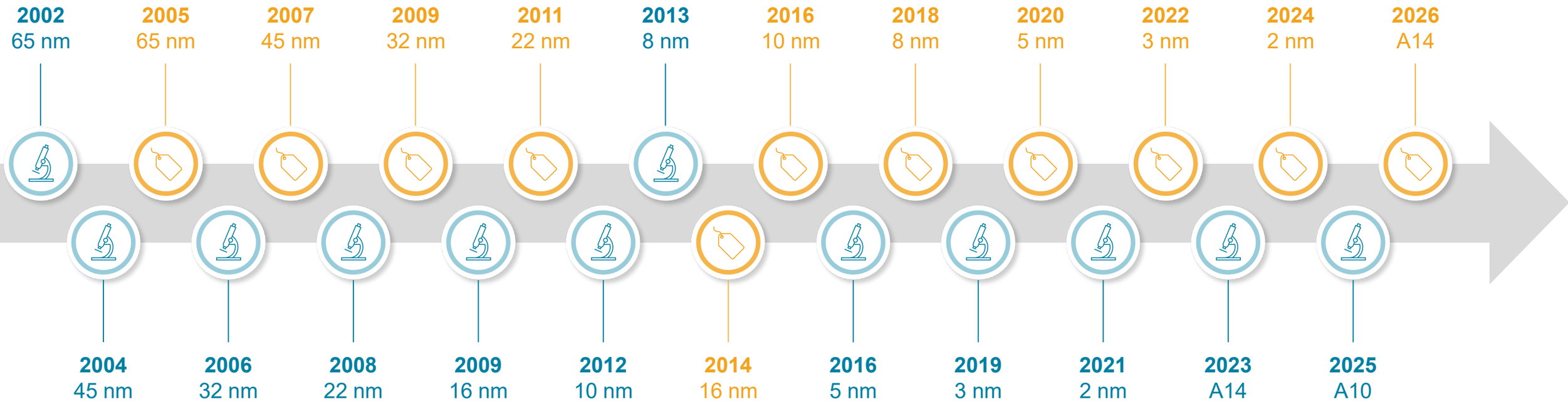
Leibniz Institute for High Performance Microelectronics
PRECISION SURFACING SOLUTIONS
Invention & Innovation NITTA
cea leti
imec
STR liquid logistics
TU BERGAKADEMIE FREIBERG
ikz LEIBNIZ-INSTITUT FÜR KRISTALLZÜCHTUNG
Fraunhofer IMW

**R&D Innovation Strategy**

- Stay one generation ahead as a **Technology Leader**
- Focus on **Leading Edge** and **Power**
- Find new business opportunities and **expand** our **product portfolio**


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# SILTRONIC HAS A PROVEN TRACK RECORD AS A TECHNOLOGY LEADER




R&D Start

Commercialization



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# SILTRONIC'S TECHNOLOGICAL LEADERSHIP AND EXCELLENCE RECOGNIZED WITH MULTIPLE AWARDS

**EPIC Supplier Award**

**Intel**
April 2025




**BEST in Value Award**

**Samsung**
September 2025




**Best Silicon Supplier Award**

**ST**
November 2025




**Best Supplier Award**

**SSMC**
August 2026




**Outstanding Quality Performance Award**

**Infineon**
September 2026




**Outstanding Supplier Performance Award**

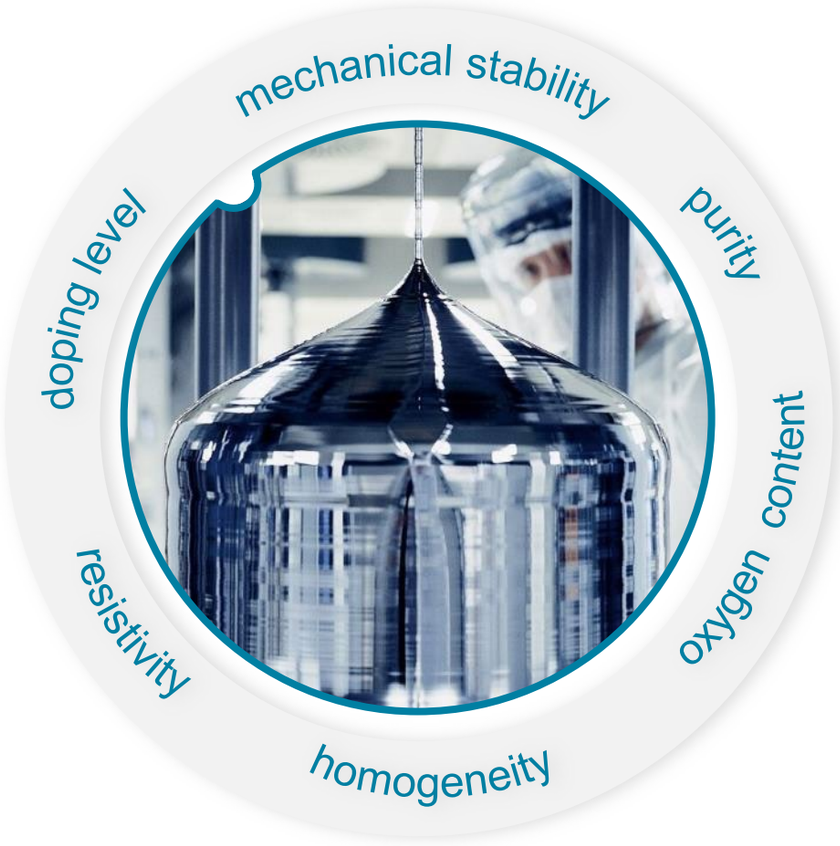
**Micron**
October 2026





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# A NUMBER OF KEY INGOT AND WAFER PROPERTIES DEFINE THE WAFER SPECIFICATION AND ITS FURTHER USE BY THE CUSTOMER




DATA








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# PURITY AND FLATNESS REQUIREMENTS FOR WAFERS ARE EXTREME, AS SCALING THEM INTO THE MACRO WORLD SHOWS

## Particles

Particle size specified in the nm range. Requirements are equivalent to a maximum of 10 grains of sand distributed over the city of Munich.



## Flatness

20 nm on a wafer are equivalent to the height of a leaf on the surface of Lake Chiemsee – or a single bacteria on a tennis court.



nm = nanometer



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# FINANCIALS AND OUTLOOK

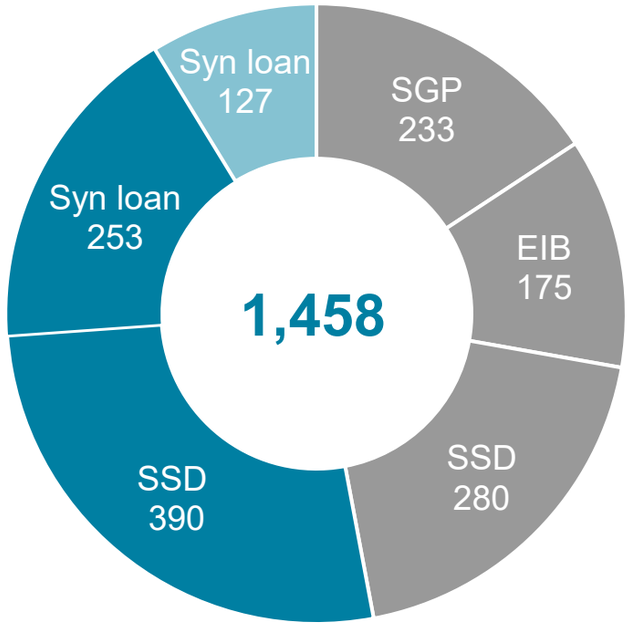




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# DEBT STRUCTURE BACKED BY STRONG LIQUIDITY

## Debt Financing Instruments
in EUR million




- Fixed loans, drawn
- Variable loans, drawn
- Syn loan, not drawn yet

Figures rounded
SSD = Schuldscheindarlehen (Promissory Note Loan)

## Maturity Profile of Debt Financing
in EUR million

| Q4 2026 | 2027 | 2028 | ∑ 2029 – 2033 |
|---|---|---|---|
| 64 | 251 | 224 | 792 |

## Cash & Securities
in EUR million

| September 30, 2026 | | Syn loan (not drawn) |
|---|---|---|
| 734 | + | 127 |

**Prepayments**
of EUR 515 mn on our balance sheet

**Net interest**
expenses of roughly EUR 30 mn in 2026


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# CAPEX SIGNIFICANTLY REDUCED SINCE PEAK IN 2023

**Capital expenditure**
in EUR million

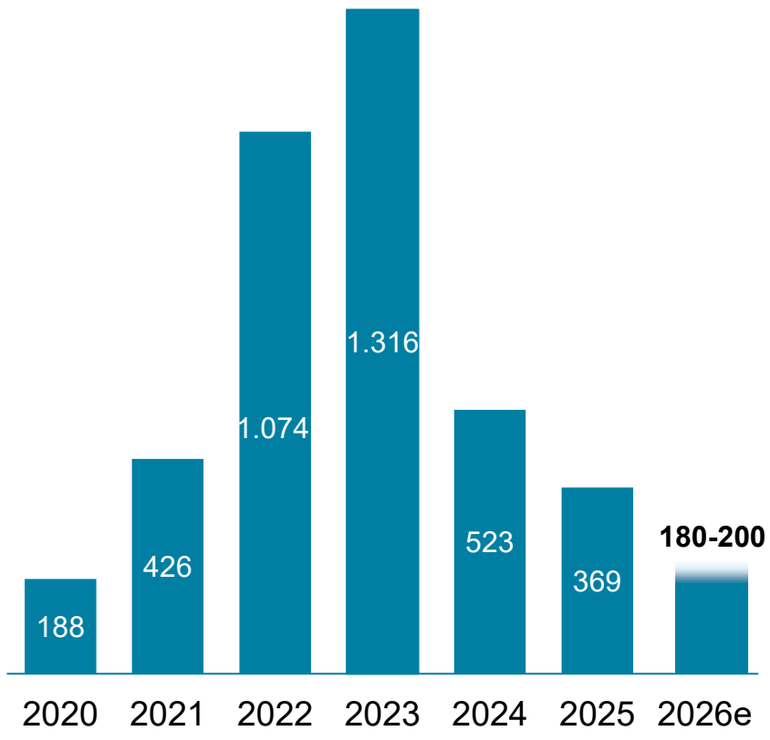



Chart not to scale

**Capital expenditure**
expected

**Brownfield capex** for capacity addition in Singapore

+

**Steady state capex**
ø EUR 200 mn p.a.



**Maintenance**
of business



**Capability**
enhancement “staying one generation ahead”



**Product mix**
ongoing improvement



**Cost efficiency**
e.g. automation


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## Major cost items 2026

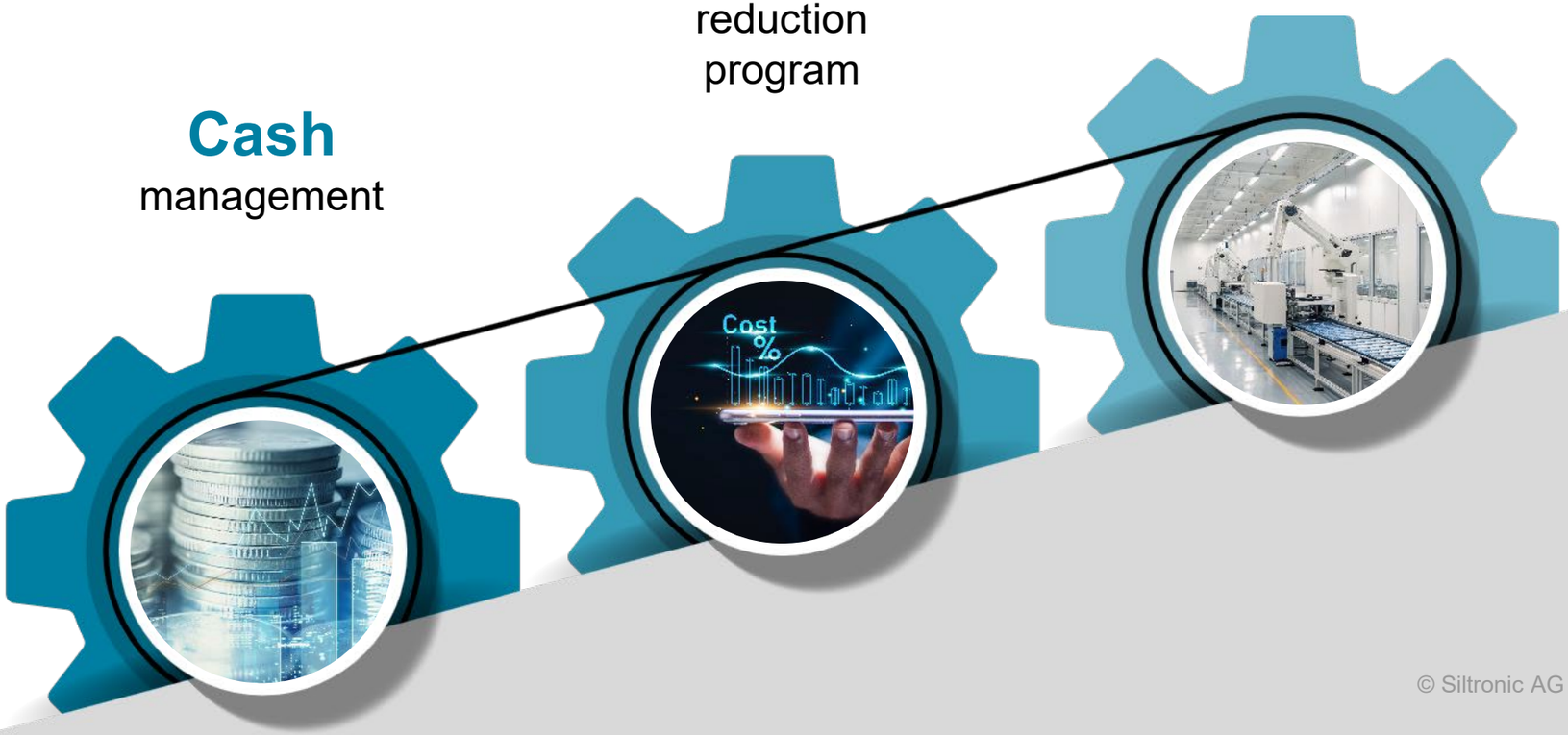
- Depreciation
- Labor
- Supplies
- Polysilicon
- Energy

# SILTRONIC WITH HIGH CASH, COST AND CAPITAL ALLOCATION DISCIPLINE

**Cash**
management

**Cost**
reduction
program

**Capital**
allocation





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# 2026 SALES GUIDANCE REVISED, EBITDA MARGIN NARROWED

## Sales

In the range of 2025[1]
Like for like above 2025[2]

## EBIT

Significantly below the previous year

## EBITDA margin

Between 22% and 24%

## Capex

Between EUR 180 and 200 mn

## Depreciation

Between EUR 480 and 500 mn

## Net Cash Flow

In the range of the previous year

[1] EUR/USD exchange rate 1.14 for Q4 2026, [2] Excluding the closure of the Small Diameter business and FX impact



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# ESG & EXECUTIVE BOARD





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# SILTRONIC IS ENABLING TECHNOLOGIES FOR A SUSTAINABLE FUTURE

## Power focus

Power chips are crucial for decarbonization and enable renewable energies and EVs

Focus on **Power** wafers
+
R&D focus in **special products and new materials**

## Leading Edge focus

New chip generations increase energy efficiency and need less power

**Leading Edge** wafers
+
R&D focus **on new Design Rules**

## Strategy

on Leading Edge and Power wafers supports sustainability strategy

## R&D focus

is crucial for sustainability strategy



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# AMBITIOUS TARGETS AND GLOBAL COMMITMENTS KEEP US GOING



**Climate – Scope 1+2**
**$CO_2$ emissions**
**2030:** -42%[1]
**2045:** Net zero[2]
**Status 2025:** -31%[1]



**Energy – renewable**
**2030:** 60% share
**2045:** 100% share
**Status 2025:** 26% share[3]

Siltronic supports the
10 principles of the **UN Global Compact**.

We contribute to the sustainable
development goals of the **United Nations**.

As a supplier to the electronics industry, Siltronic is a “Regular” member of the **Responsible Business Alliance (RBA)**.

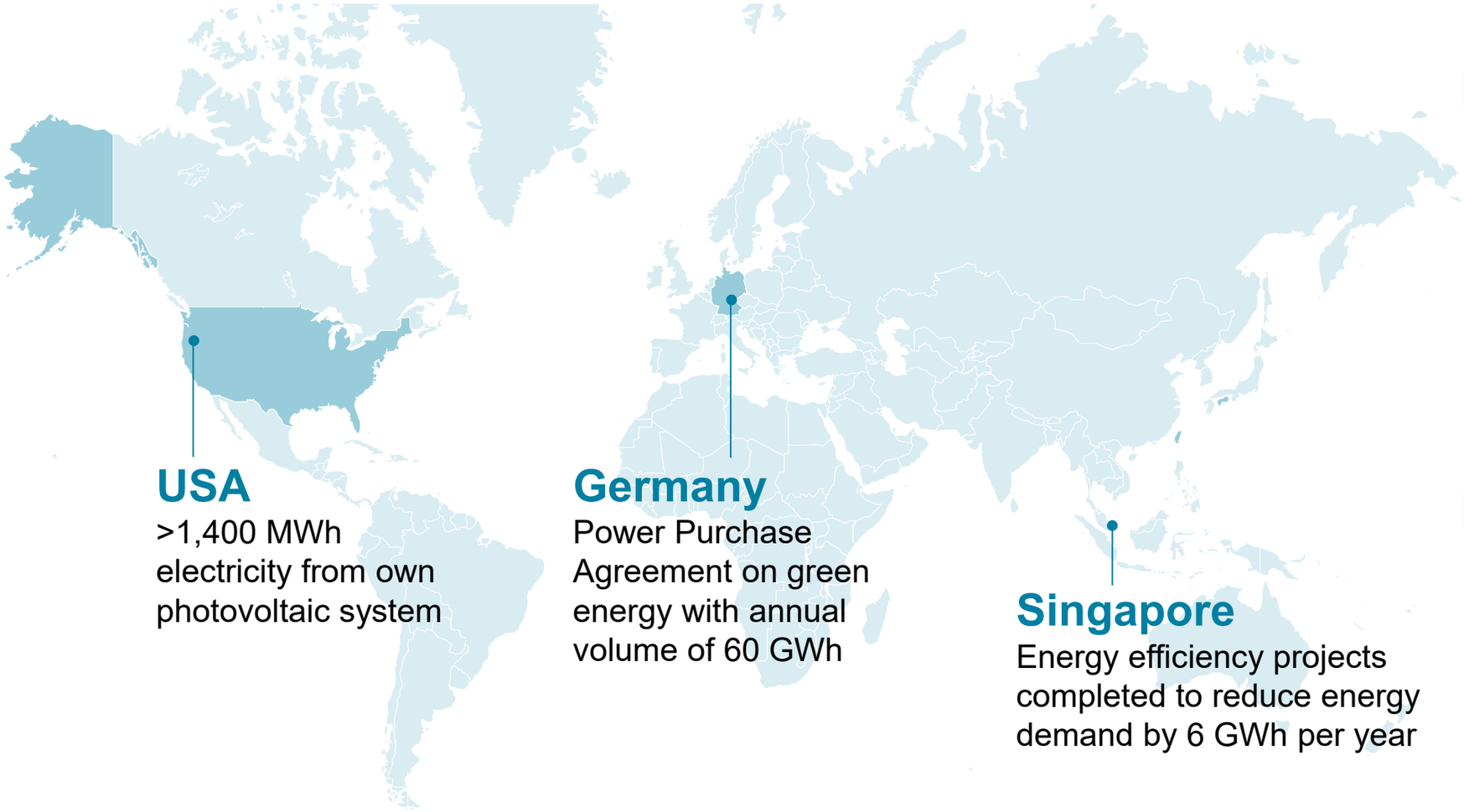
Siltronic achieved an ‘**A** – Leadership’ score in **CDP** Climate change and Water security ratings.

With our membership at **RE100**, we are dedicated to make our contribution to global decarbonization.

[1]Absolute reduction, base year 2021; [2]Net zero: GHG emissions released into the atmosphere equal the amount removed; [3]Based on RE100 criteria


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# SUSTAINABILITY HIGHLIGHTS 2025



## USA
>1,400 MWh electricity from own photovoltaic system

## Germany
Power Purchase Agreement on green energy with annual volume of 60 GWh

## Singapore
Energy efficiency projects completed to reduce energy demand by 6 GWh per year

## CDP A list
'A-Leadership' score in CDP Climate Change and Water Security rating for the first time in 2025

## -31% $CO_2$
Reduction of absolute $CO_2$ scope 1+2 emissions in 2025 compared to base year 2021


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# THE SILTRONIC EXECUTIVE BOARD



**Dr. Michael Heckmeier**
CEO

- CEO since May 2023
  (appointed until May 5, 2031)
- 25 years at Merck
- Studied physics and mathematics, PHD in physics
- MBA, General Management



**Claudia Schmitt**
CFO

- CFO since July 2023
  (appointed until June 30, 2031)
- 16 years at Siltronic
- 11 years at Wacker Chemie AG
- Studied Business Administration



**Klaus Buchwald**
COO

- COO since June 2024
  (appointed until May 31, 2032)
- 21 years at Infineon
- 4 years at Rohde & Schwarz
- Studied Mechanical and Industrial Engineering


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# EXECUTIVE SUMMARY





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# KEY TAKEAWAYS – CLEAR COMMITMENT TO PROFITABLE GROWTH AS A TECHNOLOGY LEADER

**Proven success** in a cyclical industry

Megatrends **AI, Digitalization, and Electromobility** will drive wafer demand to new levels




Additional production capacities in **Singapore** will improve our position and drive growth

Clear commitment to **R&D, Sustainability and Innovation** to stay ahead of the curve


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# Financial Calendar and Events

November 9, 2026
**BNP Paribas Roadshow, Paris**

November 11, 2026
**Metzler Frankfurt Roadshow**

November 20, 2026
**Morgan Stanley TMT Conference, Barcelona**

November 23, 2026
**Deutsches Eigenkapitalforum Conference, Frankfurt**

November 25, 2026
**GS European AI & Semis Symposium (virtual)**

November 30, 2026
**Berenberg European Conference, London**

December 1, 2026
**BofA European Tech Fieldtrip (virtual)**

# Investor Relations Contact



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