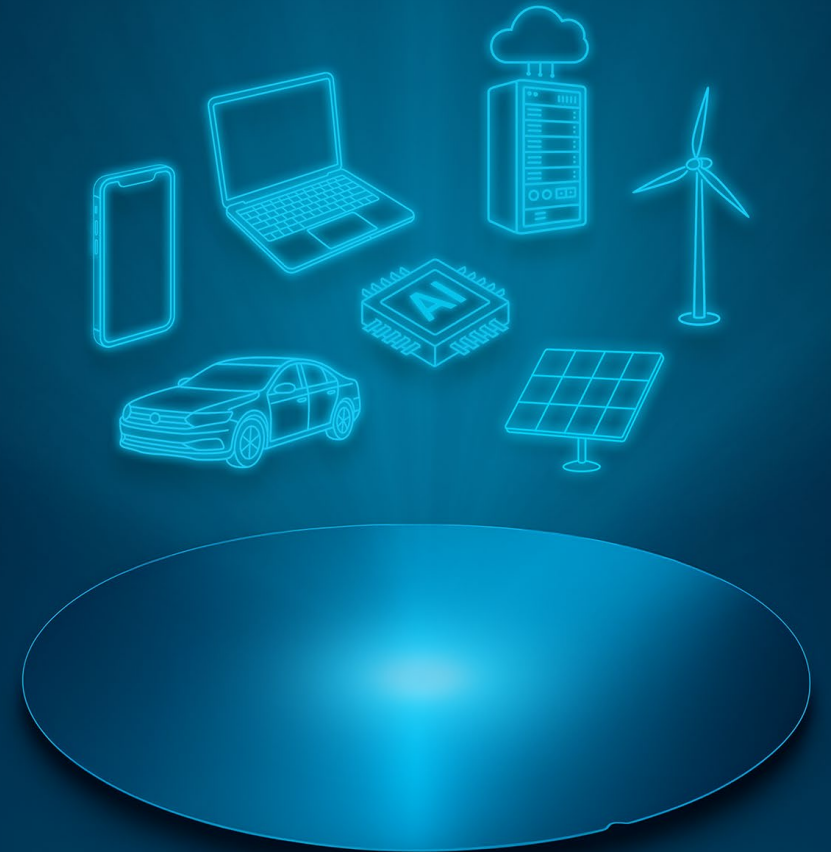




FOUNDATION OF DIGITAL LIFE



Investor Relations
September 1, 2026

SILTRONIC AT A GLANCE

4,300

employees
worldwide

4

world-class
production sites

1.35

billion EUR
sales in 2025

50+

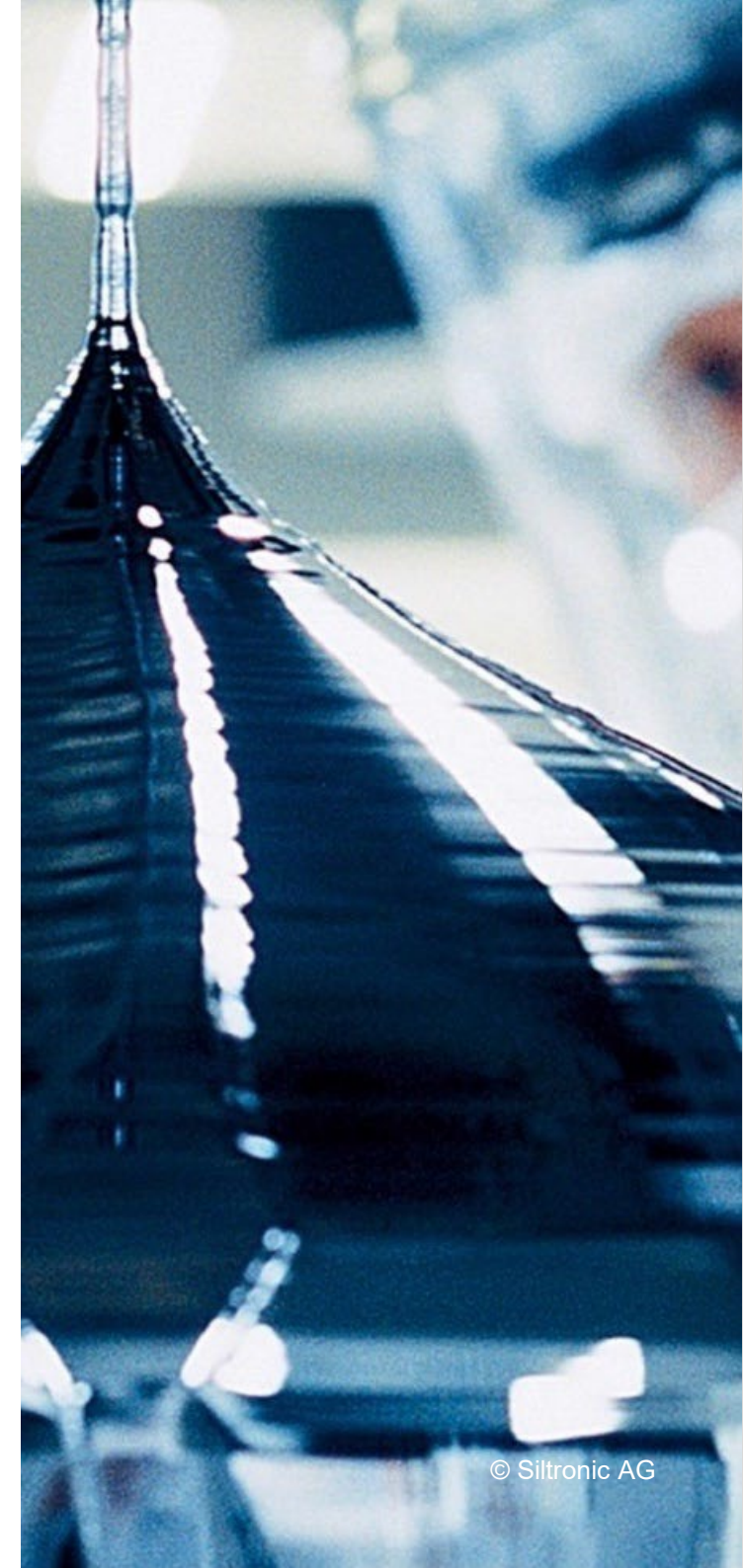
years of history
in silicon technologies

Supplier

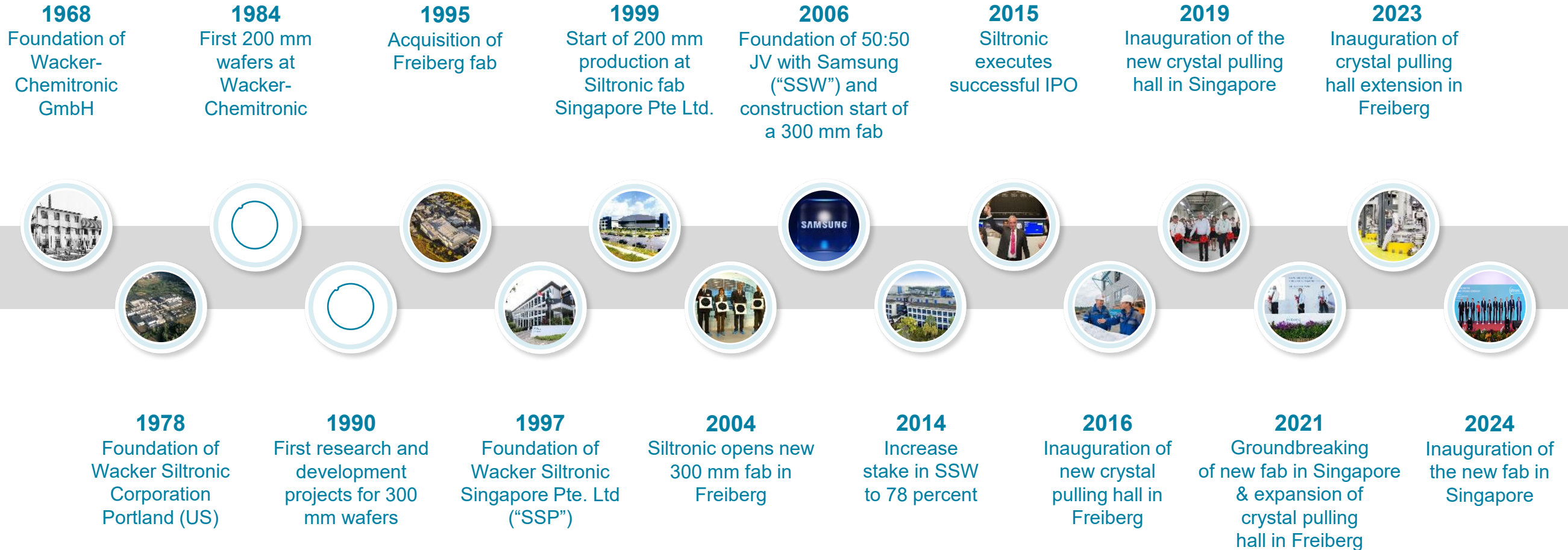
to top
semiconductor producers

23.5

percent
EBITDA margin in 2025



50+ YEARS OF HISTORY – A STORY OF SUCCESS AND INNOVATION



OUR INTERNATIONAL PRODUCTION NETWORK SERVES CUSTOMERS AROUND THE GLOBE

Singapore



Singapore



SSW*



300
mm

SSP

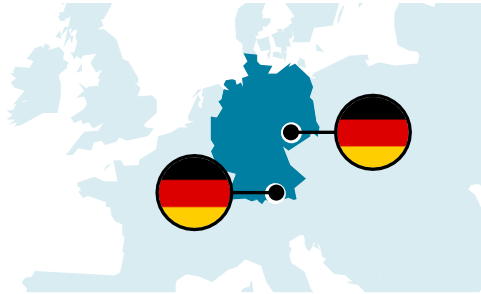
200
mm



300 mm

volume production in
Germany & Singapore

Germany



Germany (Burghausen, Freiberg)



CP

300
mm



R&D

hub in Burghausen

USA



USA (Portland / Oregon)



200
mm

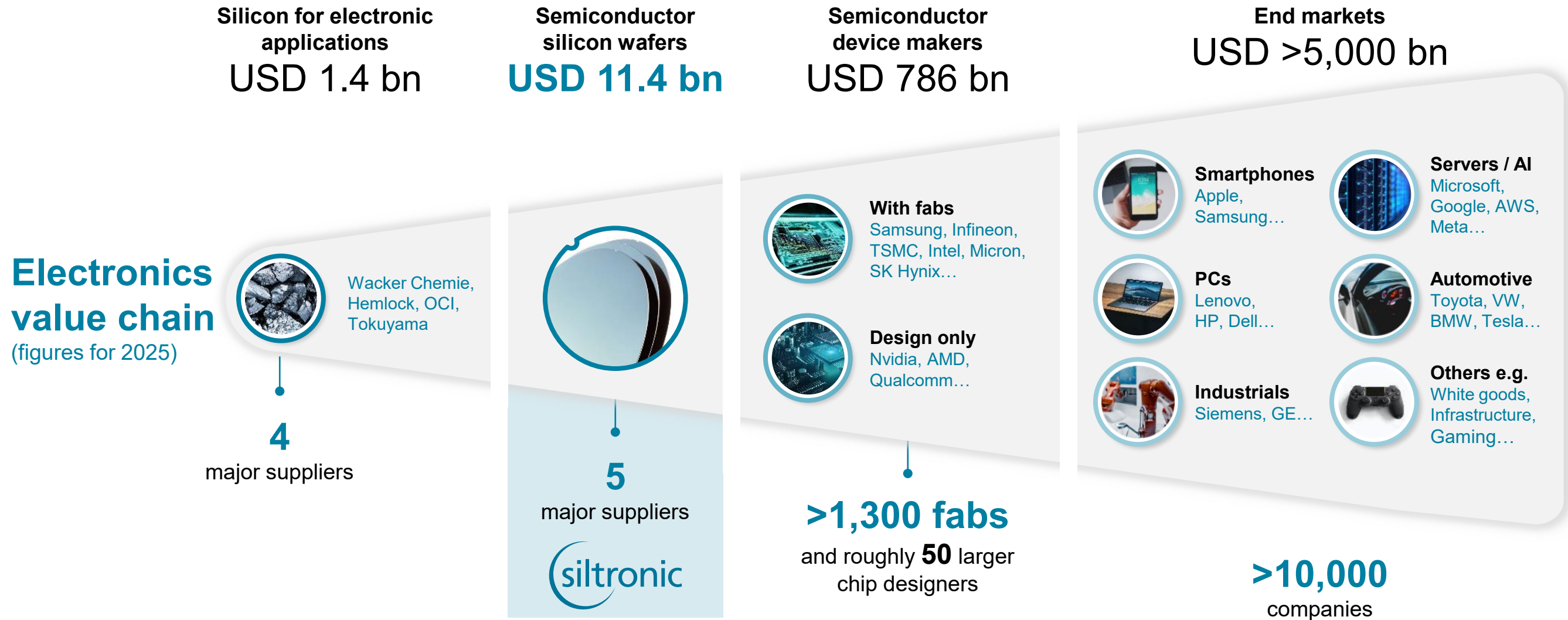


Leading Edge production

*JV with Samsung Electronics

JV = Joint Venture
CP = Crystal Pulling
R&D = Research and Development

THE WAFER SUPPLIERS ARE FUELING THE WHOLE ELECTRONICS VALUE CHAIN



Source: TechInsights & other companies, WSTS (Silicon based), SEMI SMG, Siltronic Marketing

SILTRONIC IS WELL POSITIONED AS THE ONLY WESTERN-BASED WAFER MANUFACTURER



Around 75%
of the market is
served by the Top 5
wafer manufacturers

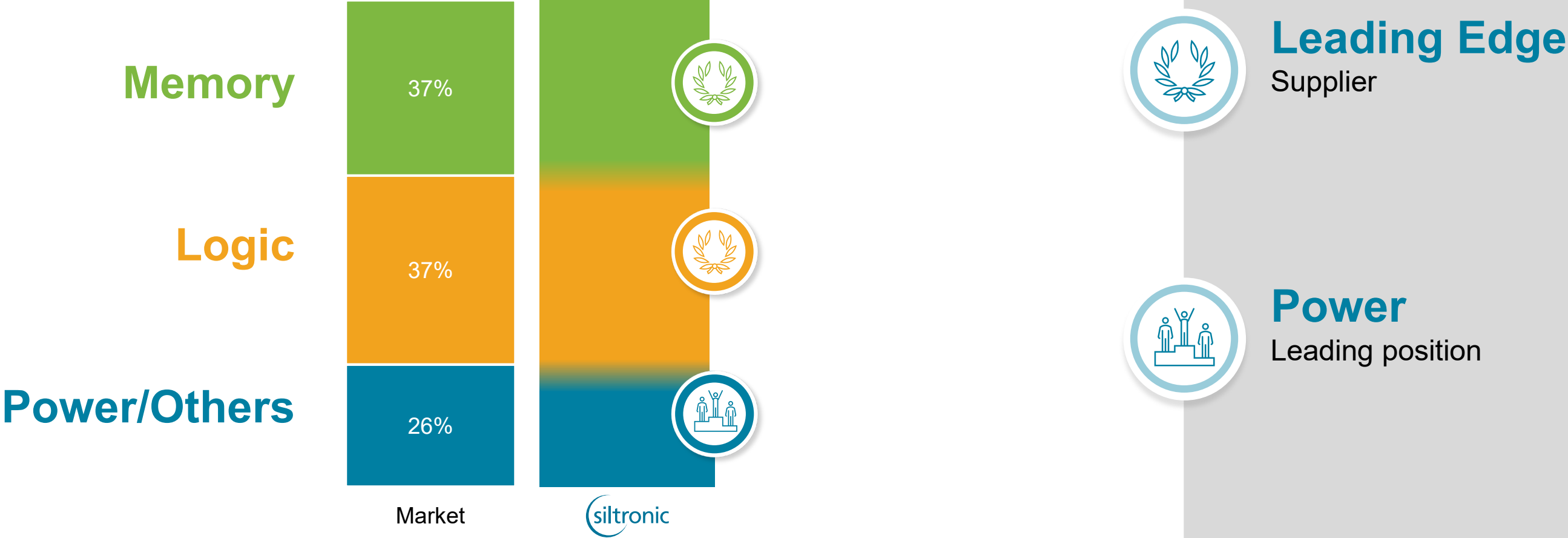


Only western
based wafer
manufacturer

Source: Q1 – Q3 2025 Quarterly revenue reports by SEMI reporting companies with Siltronic estimates on silicon wafer revenues

SILTRONIC HAS A WELL-DIVERSIFIED AND RESILIENT PRODUCT MIX IN ALL THREE SEMI-SEGMENTS

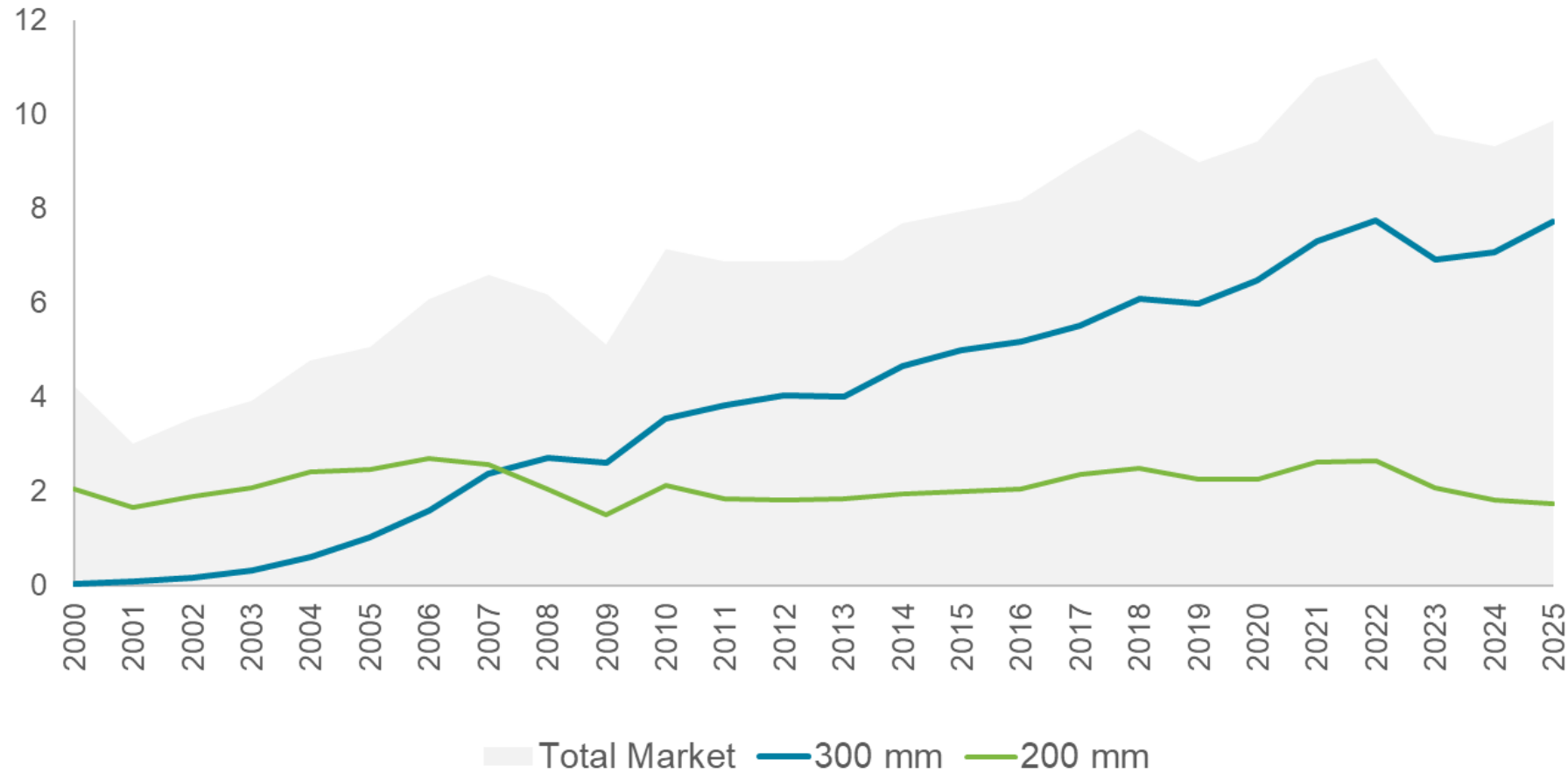
Demand by segments in % of total demand in 2025



Source: Siltronic Marketing

WAFER DEMAND GROWTH EXPECTED TO CONTINUE (CAGR¹ 5-6%), DRIVER: 300 mm WAFERS

Wafer Demand per Diameter in mn 300 mm equivalents



Source: Siltronic estimates excluding China; ¹CAGR = Compound Annual Growth Rate (2000-2025)



CAGR 6%
for 300 mm

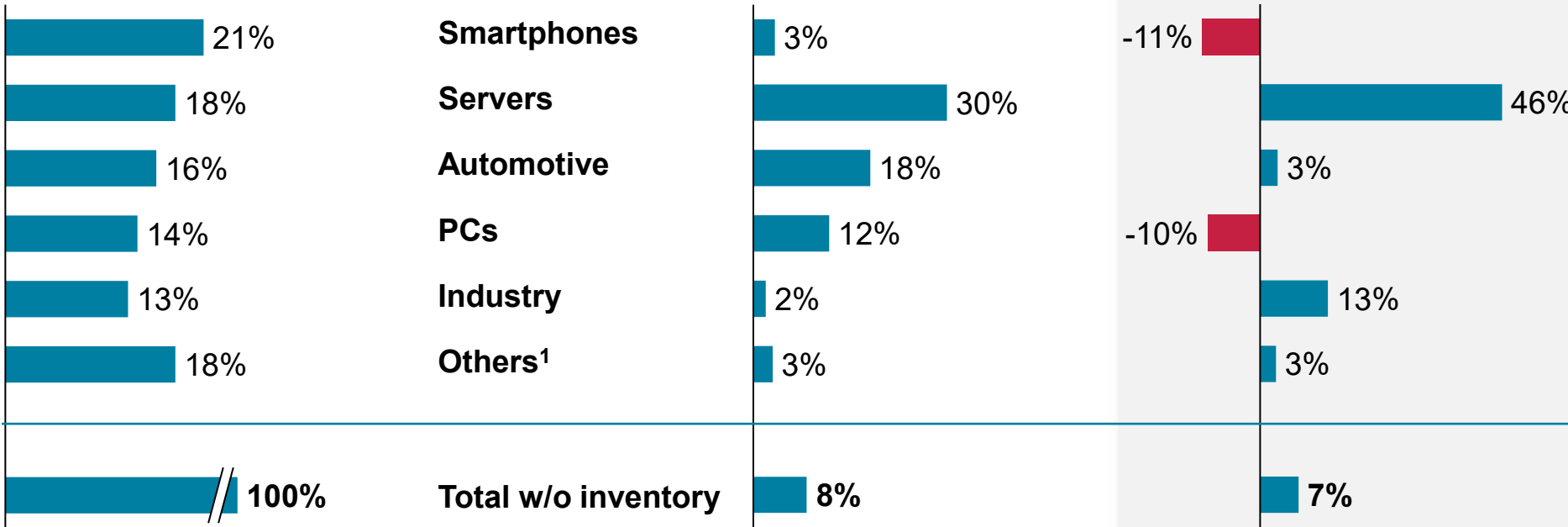


CAGR 1%
for 200 mm

END MARKETS (PRE INVENTORY) EXPECTED TO GROW BY 7 PERCENT

Growth in Wafer Area Consumption, in % yoy

Split wafer consumption by end use in 2025



Inventory impact on wafer demand



Memory rebuilding, Power elevated



Server
driven by AI



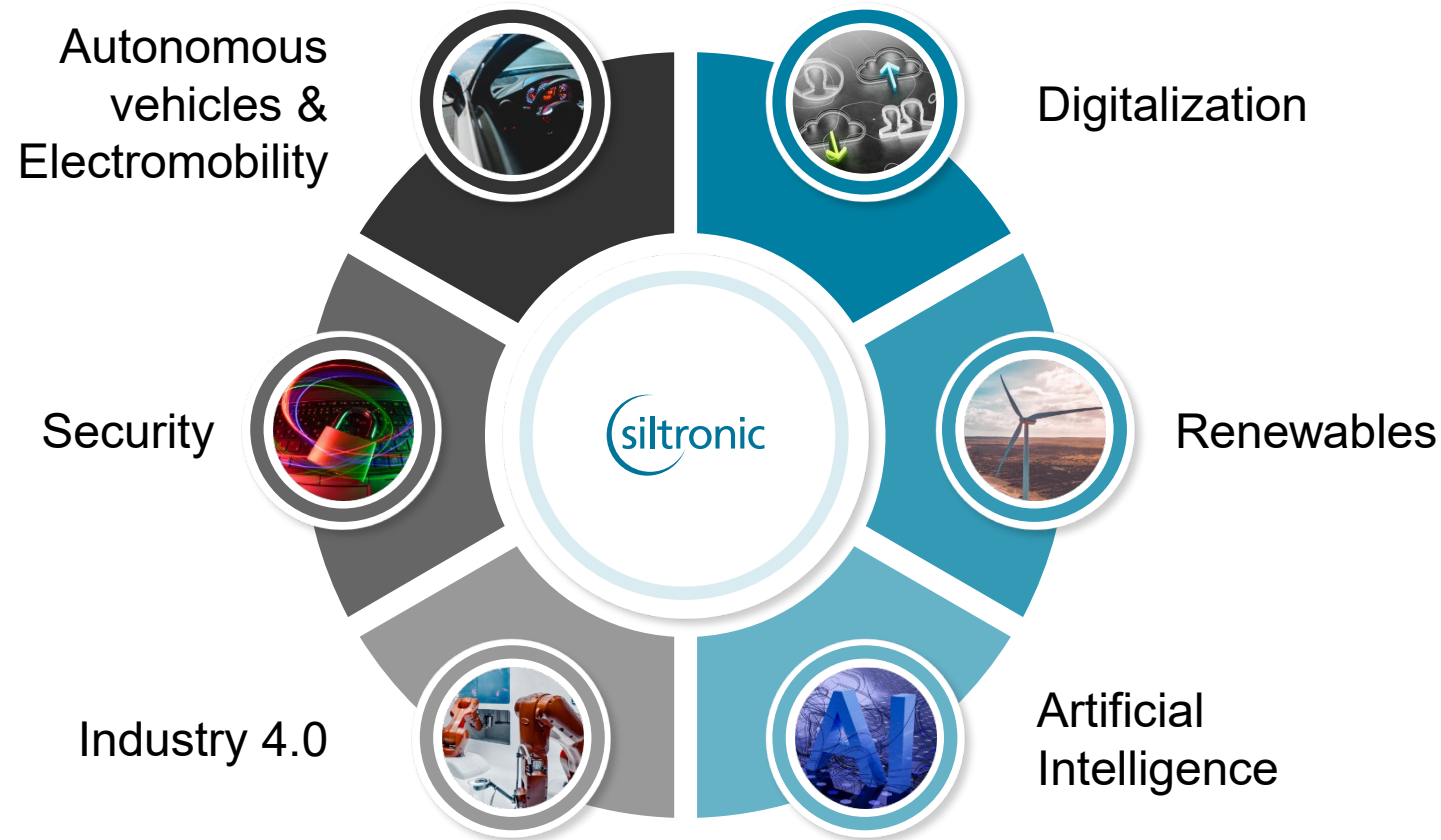
Cars
with continued moderate growth



Industry
gaining further momentum

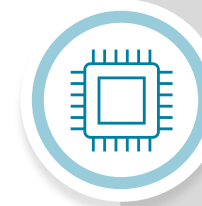
¹Infrastructure Telecommunications, game consoles, consumer electronics such as televisions, white goods such as refrigerators
Source: Siltronic estimates as of June 2026

MEGATRENDS CONTINUE TO DRIVE WAFER DEMAND



Siltronic

is well positioned to support each megatrend



All segments

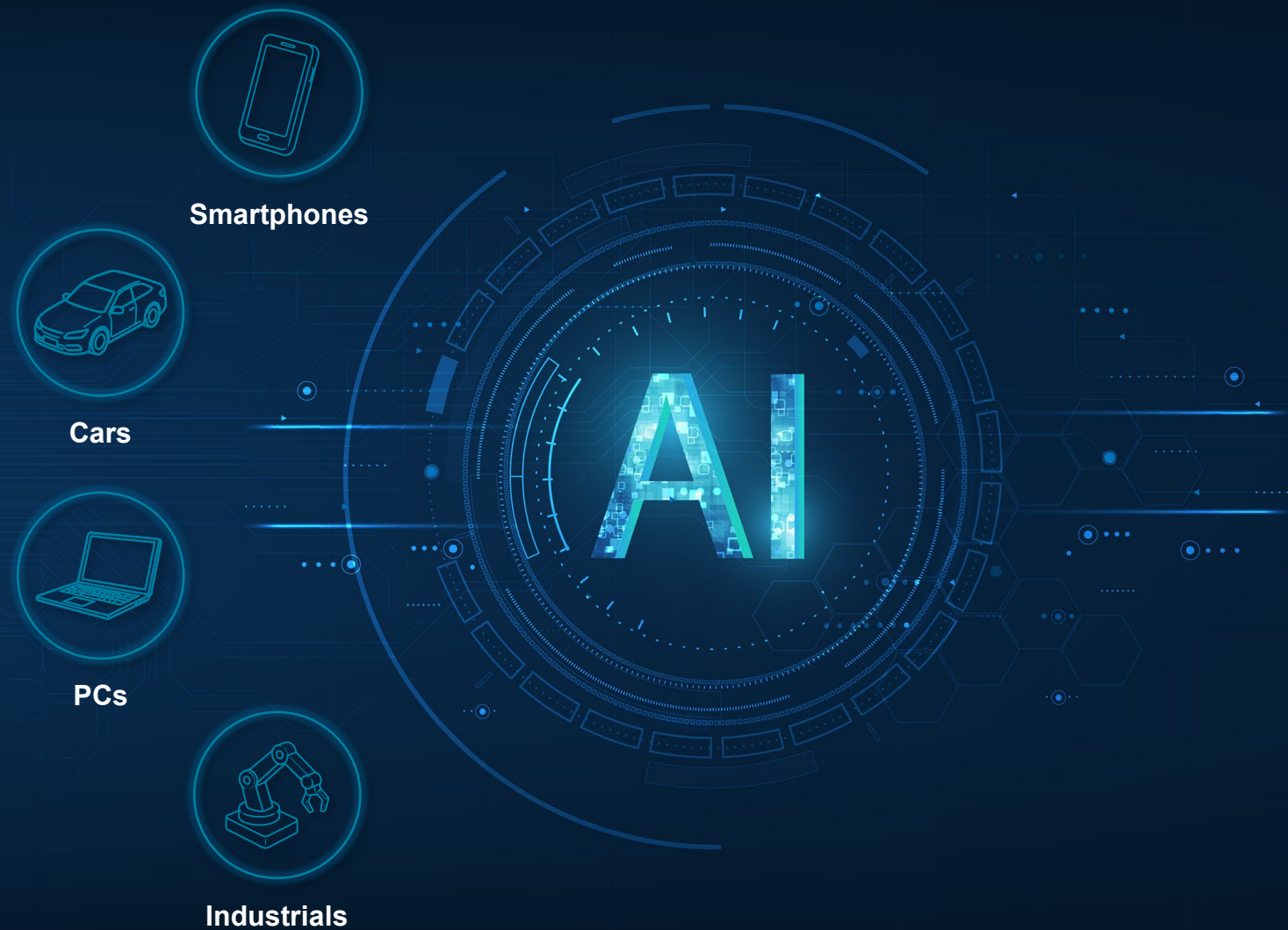
are growing



Key benefits

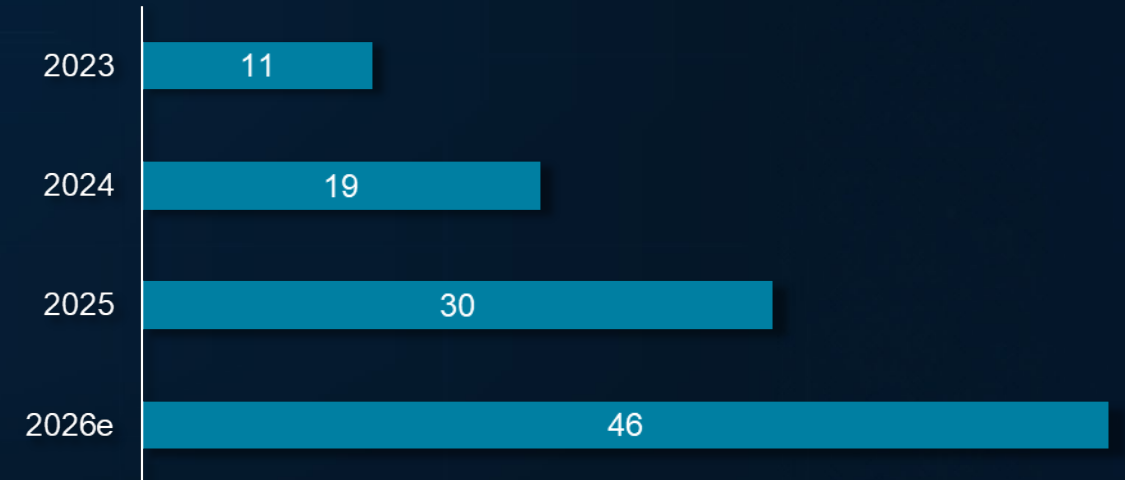
from strategic focus on Leading Edge and Power market

AI IS DRIVING WAFER DEMAND ACROSS ALL END MARKETS, WITH STRONGEST IMPACT IN SERVERS

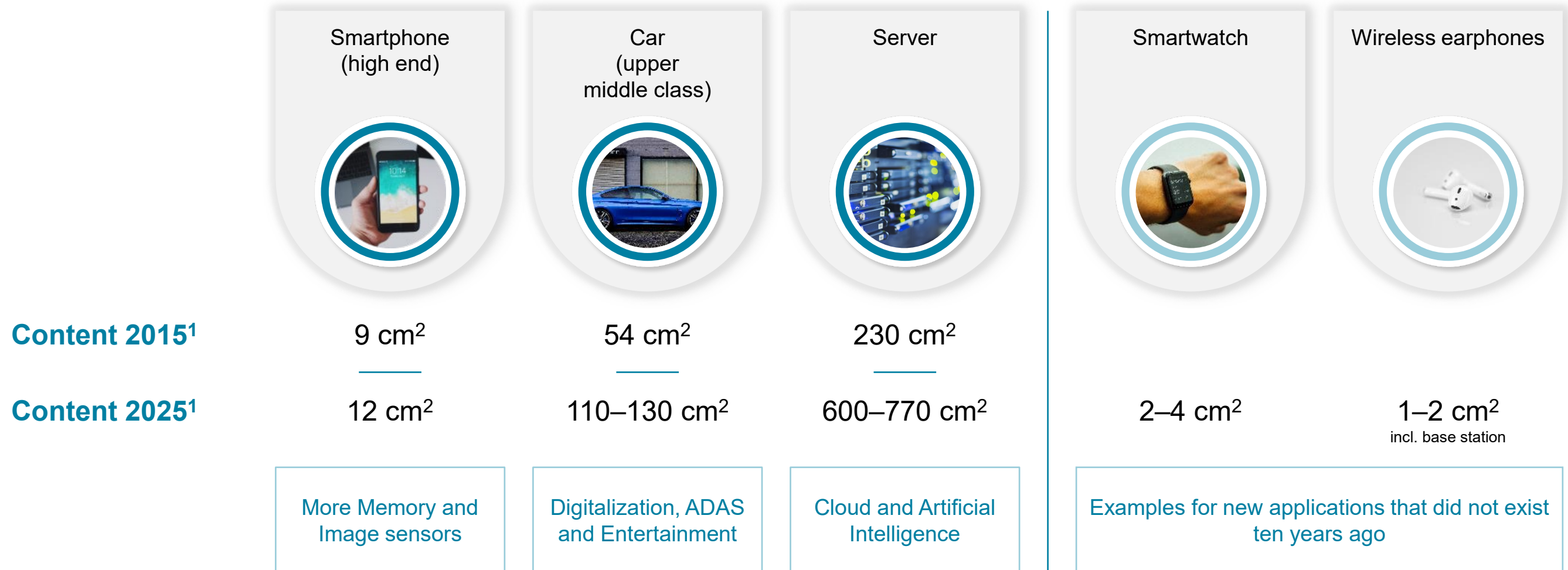


Servers

Growth in wafer area consumption (% yoy)



AND CONTENT IN END PRODUCTS IS GROWING WITH NEW PRODUCTS ENTERING THE MARKET



Source: Siltronic estimates, ¹ Silicon wafer area per device

ADDITIONAL SINGAPORE CAPACITY SUPPORTS DEMAND



Fab **operating as planned** and serving Leading Edge customers

FAB HIGHLIGHTS



EBITDA further developing towards group margin level



Targeted mid-term **EBITDA margin: 50%+**



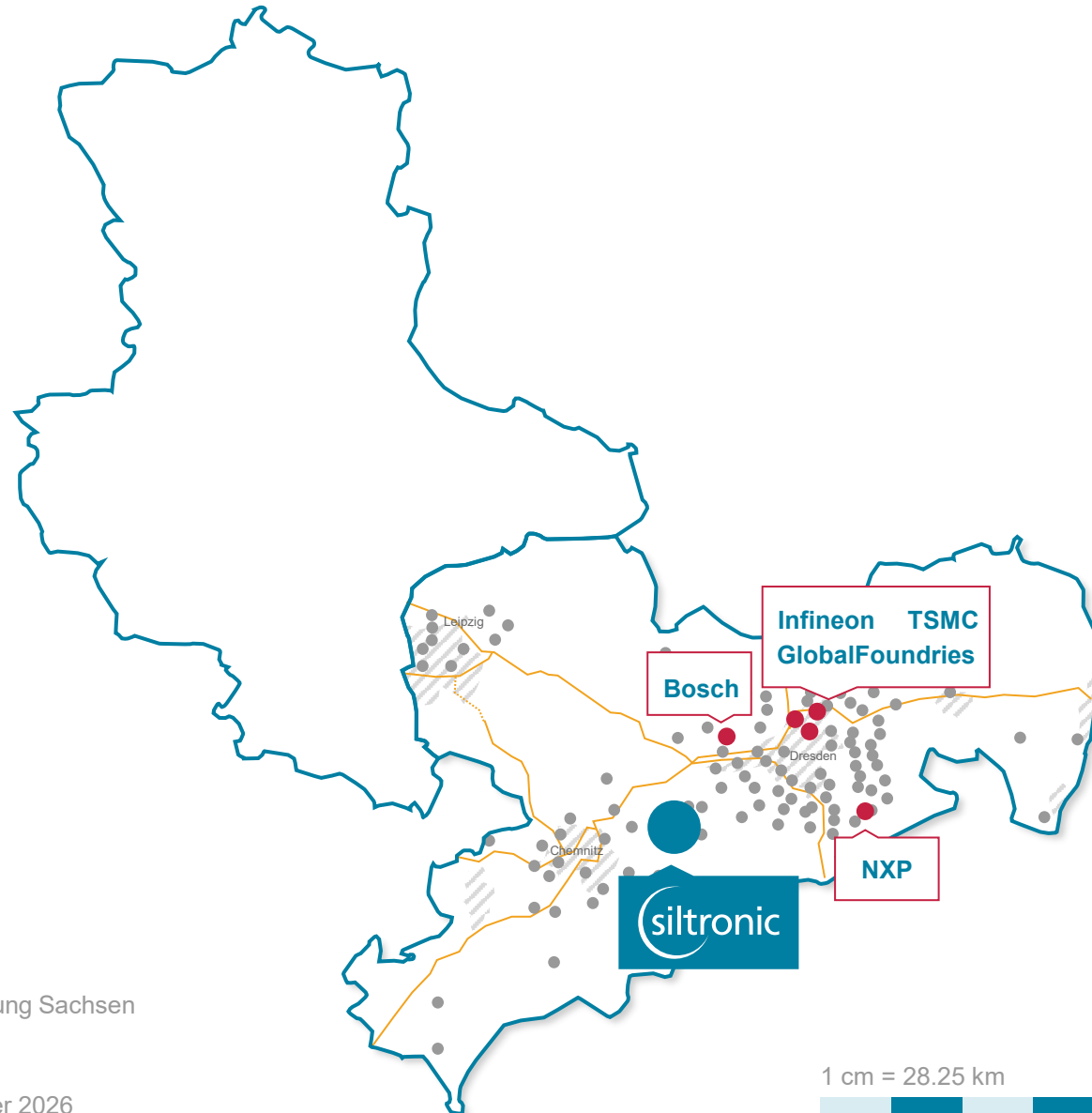
Highly **automated** and substantial **epi share**, with high **economies of scale**



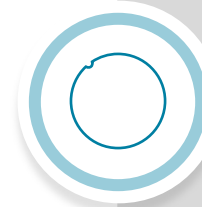
Further ramp depending on market development



IN FREIBERG WE FURTHER IMPROVED PRODUCT MIX FUELED BY INVESTMENTS



Source: Siltronic; Silicon Saxony/Wirtschaftsförderung Sachsen



**Every
2nd to 3rd**
semiconductor in the
EU is from this region

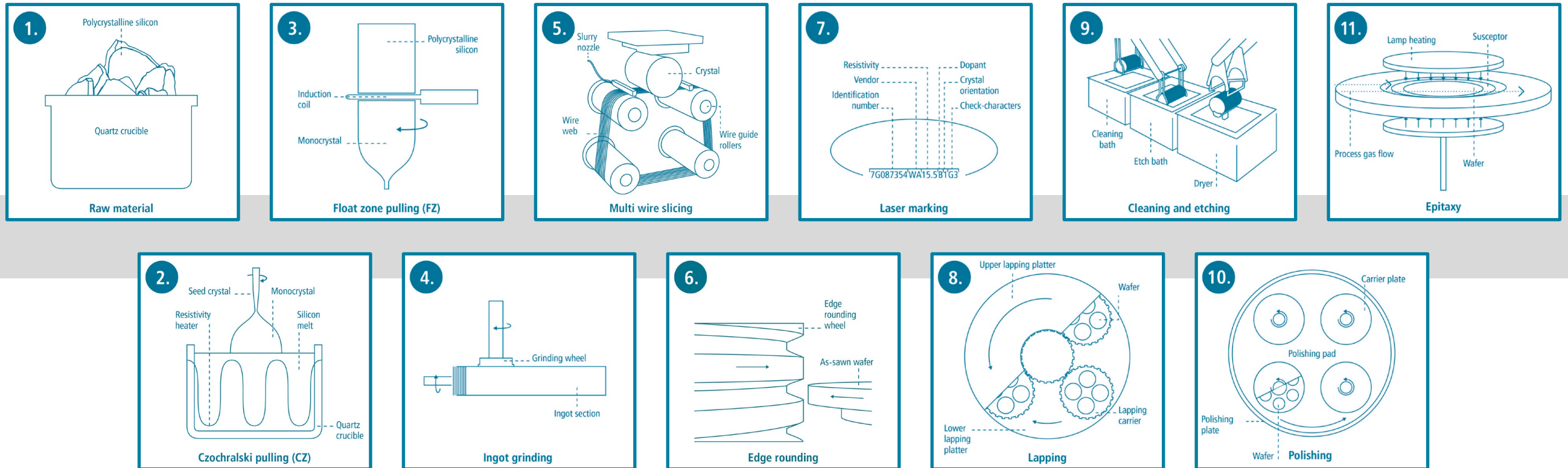


Investments
of more than EUR 1 bn
since 1995 by Siltronic



First-class
geographical position
in light of EU ambitions

WAFER PRODUCTION PROCESS



STRONG FOCUS ON GROWTH DRIVERS R&D AND INNOVATION

R&D

hub in Burghausen

450

R&D employees

1,900

patents

5-6%

of sales planned
as R&D spendings



Partnering with leading innovation
platforms



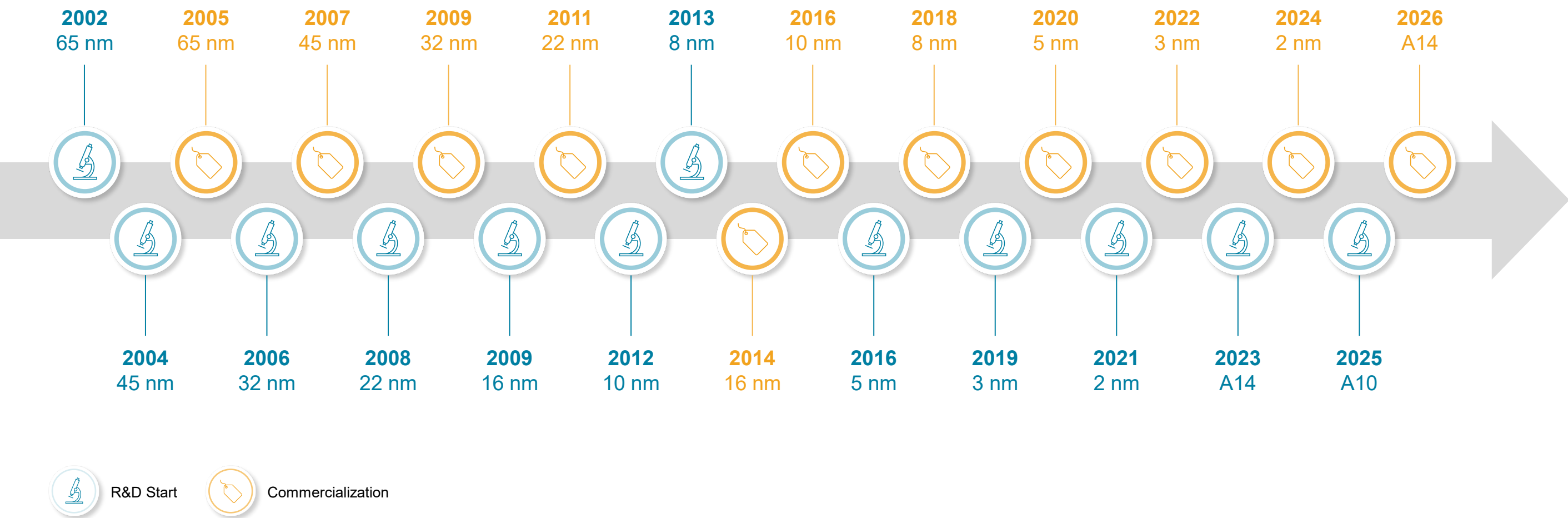
Leibniz Institute
for High
Performance
Microelectronics



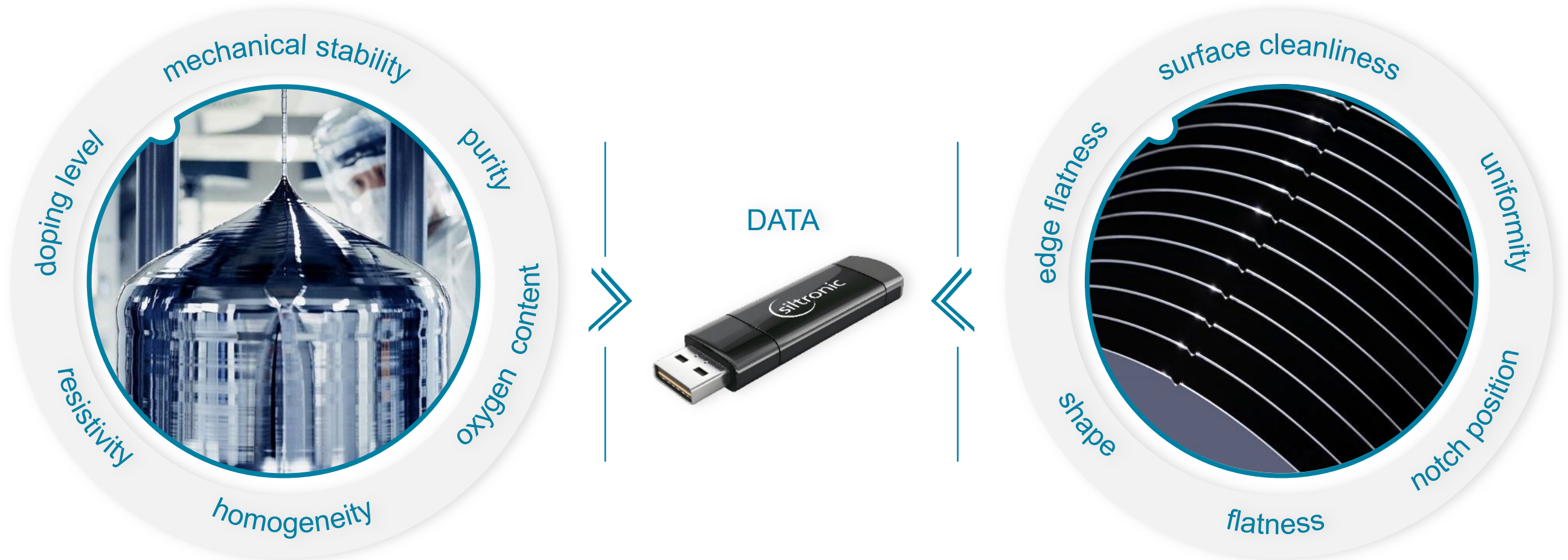
R&D Innovation Strategy

- Stay one generation ahead
as a **Technology Leader**
- Focus on **Leading Edge** and **Power**
- Find new business opportunities and
expand our **product portfolio**

SILTRONIC HAS A PROVEN TRACK RECORD AS A TECHNOLOGY LEADER



A NUMBER OF KEY INGOT AND WAFER PROPERTIES DEFINE THE WAFER SPECIFICATION AND ITS FURTHER USE BY THE CUSTOMER



PURITY AND FLATNESS REQUIREMENTS FOR WAFERS ARE EXTREME, AS SCALING THEM INTO THE MACRO WORLD SHOWS

Particles

Particle size specified in the nm range. Requirements are equivalent to a maximum of 10 grains of sand distributed over the city of Munich.



nm = nanometer

Flatness

20 nm on a wafer are equivalent to the height of a leaf on the surface of Lake Chiemsee – or a single bacteria on a tennis court.



FINANCIALS AND OUTLOOK

SUCCESSFUL CAPITAL INCREASE



SIZE

EUR 273 mn

Gross issuance proceeds



CAPITAL INCREASE

10%

of existing share capital
(3.0m new shares)



PRICE

EUR 91.00

~7% discount to prior day's
closing price



METHOD

ABB

Accelerated Bookbuilding,
subscription rights excluded

Use of Proceeds

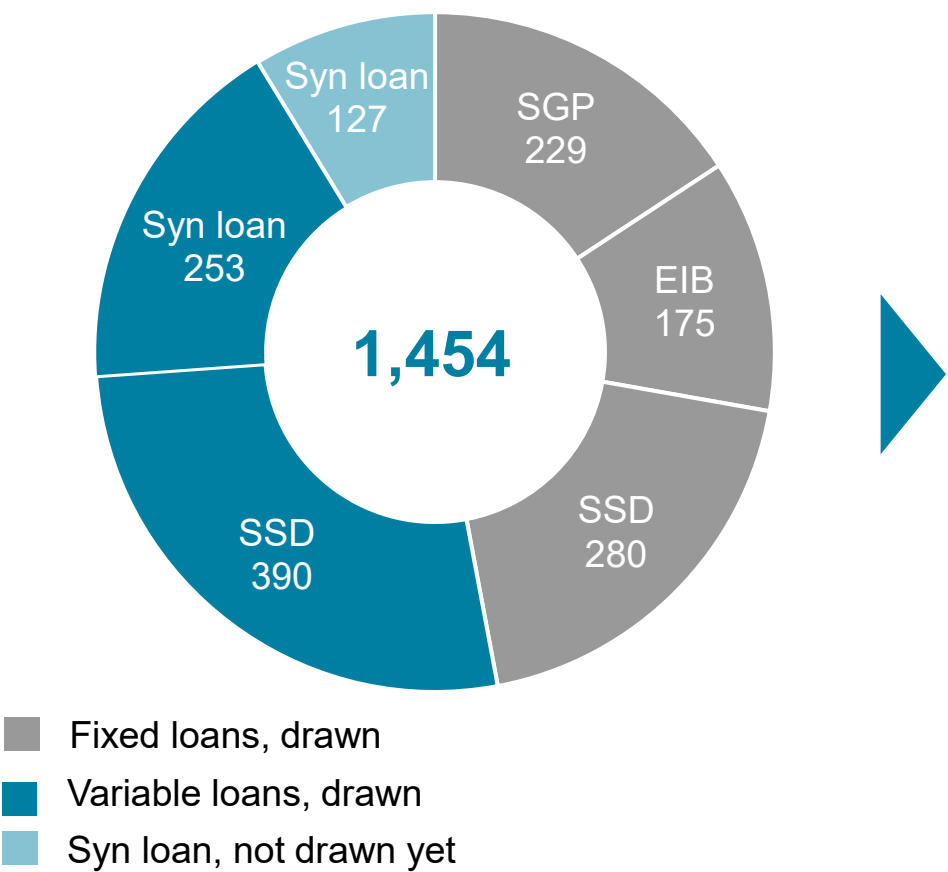
- Supporting long-term growth and capturing market opportunities
- Strengthening the balance sheet and increasing financial flexibility

Investor Base Quality

- High participation from new and existing long-only investors
- Anchor shareholder HAL supported the transaction with a meaningful order
- Strong demand among European and US investors

STRONG LIQUIDITY POSITION AND BALANCED DEBT MATURITY PROFILE

Debt Financing Instruments
in EUR million



Figures rounded
SSD = Schuldscheindarlehen (Promissory Note Loan)

Maturity Profile of Debt Financing
in EUR million

H2 2026	2027	2028	Σ 2029 – 2033
63	250	220	790

Cash & Securities
in EUR million

June 30, 2026	Syn loan (not drawn)
650	127



Prepayments
of around EUR 10 mn expected to be refunded in 2026



Interest
expenses in the ballpark of EUR 50 mn in 2026

CAPEX SIGNIFICANTLY REDUCED SINCE PEAK IN 2023

Capital expenditure
in EUR million

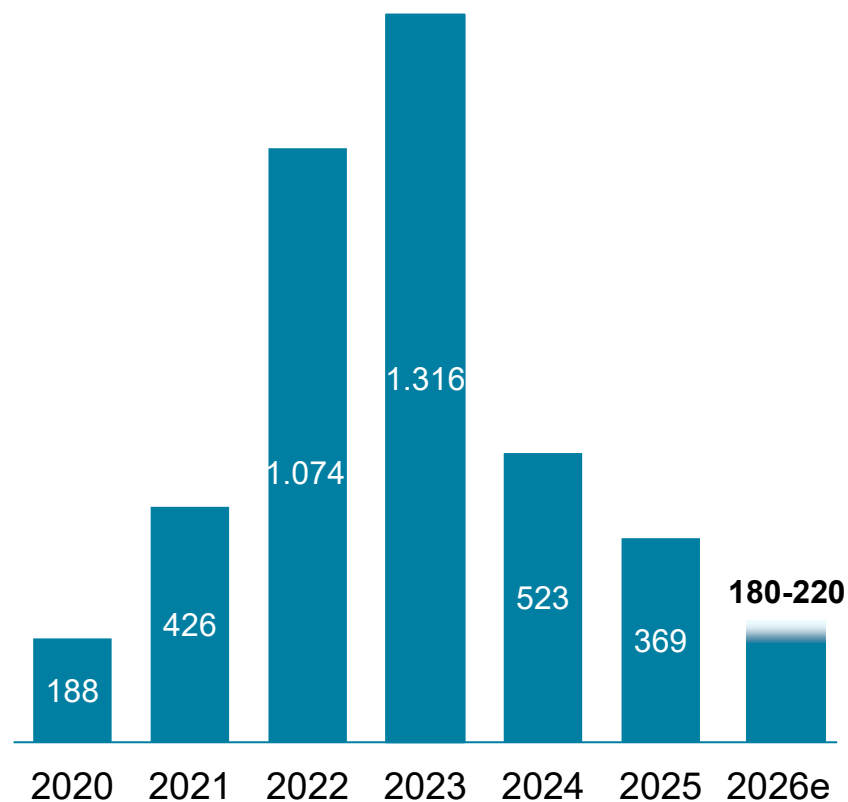


Chart not to scale

Capital expenditure
expected



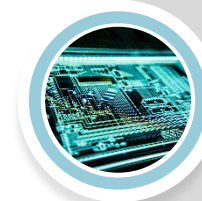
Brownfield capex for
the new Singapore fab



Steady state capex
Ø EUR 200 mn p.a.



Maintenance
of business



Capability
enhancement “staying
one generation ahead”



Product mix
ongoing improvement



**Cost
efficiency**
e.g. automation

SILTRONIC WITH HIGH CASH, COST AND CAPITAL ALLOCATION DISCIPLINE

Depreciation



Labor



Supplies



Polysilicon



Energy



Cash
management



Cost
reduction
program



Capital
allocation



SILTRONIC'S TECHNOLOGICAL LEADERSHIP AND EXCELLENCE RECOGNIZED WITH MULTIPLE AWARDS



**EPIC Supplier
Award**

Intel, April 2025



**BEST in Value
Award**

Samsung, September 2025



**Best Supplier
Award**

SSMC, September 2025



**Outstanding
Supplier
Performance
Award**

Micron, November 2025



**Best Silicon
Supplier Award**

ST, November 2025



2026 SALES GUIDANCE EDGED UP

Sales

Low to mid-single digit below 2025¹
like for like on or slightly above 2025²

EBITDA margin

Between 20% and 24%

Depreciation

Between EUR 490 and 520 mn

EBIT

Significantly below the previous year

Capex

Between EUR 180 and 220 mn

Net Cash Flow

In the range of the previous year

¹ EUR/USD exchange rate 1.18 for H2 2026, ² Excluding the impact of the closure of the Small Diameter business and FX impact

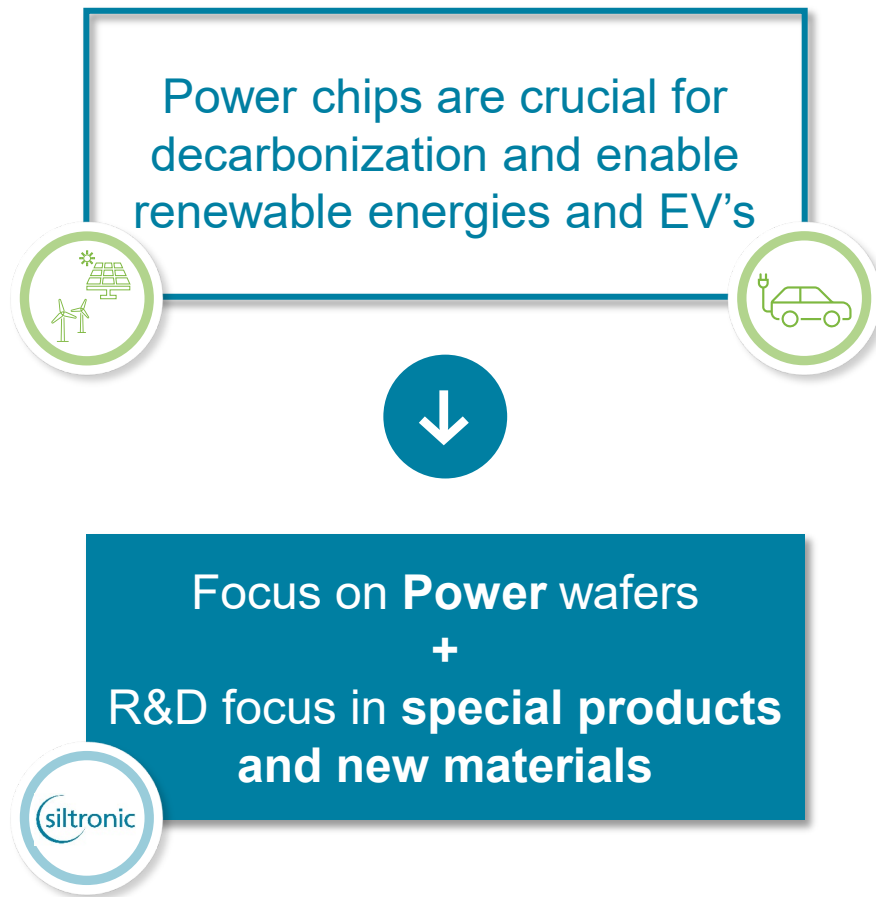


ESG @ SILTRONIC

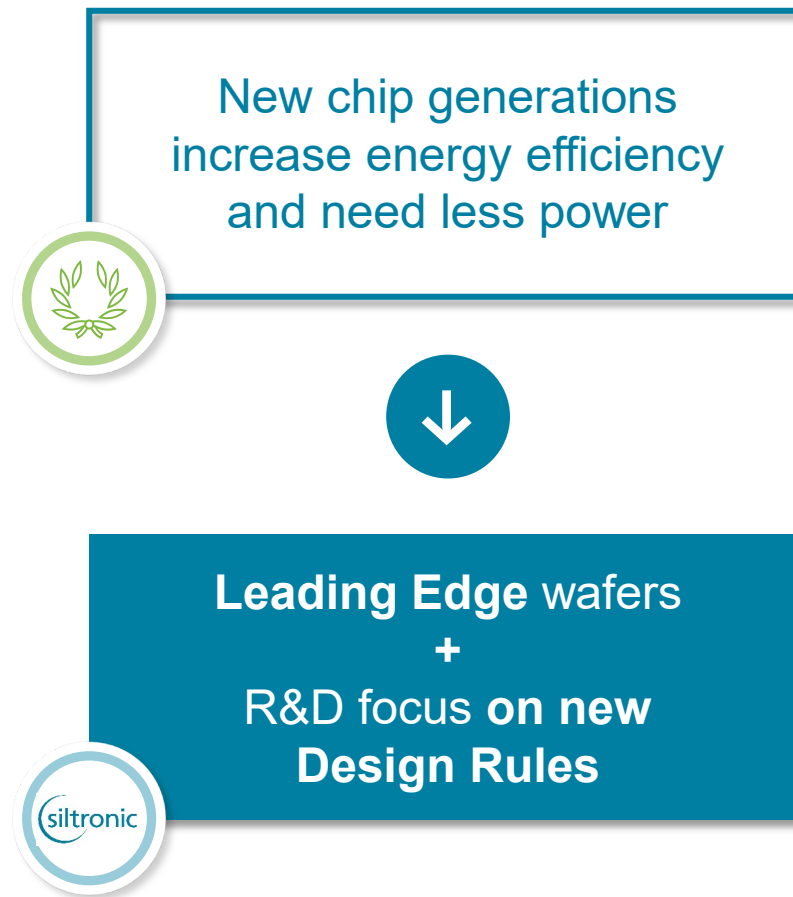
Clear commitment to an emission-free future

SILTRONIC IS ENABLING TECHNOLOGIES FOR A SUSTAINABLE FUTURE

Power focus



Leading Edge focus



Strategy

on Leading Edge and Power wafers supports sustainability strategy



Investments

needed to keep our equipment state of the art for Leading Edge wafers



R&D focus

is crucial for sustainability strategy

AMBITIOUS TARGETS AND GLOBAL COMMITMENTS KEEP US GOING

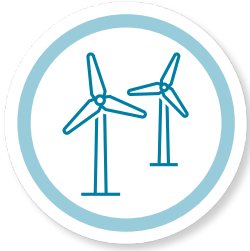


Climate – Scope 1+2 CO₂ emissions

2030: -42%¹

2045: Net zero²

Status 2025: -31%¹



Energy – renewable

2030: 60% share

2045: 100% share

Status 2025: 26% share³



Siltronic supports the
10 principles of the **UN Global Compact**.



We contribute to the sustainable
development goals of the **United Nations**.



Responsible Business Alliance
Advancing Sustainability Globally

As a supplier to the electronics industry, Siltronic is
a “Regular” member of the **Responsible Business
Alliance (RBA)**.



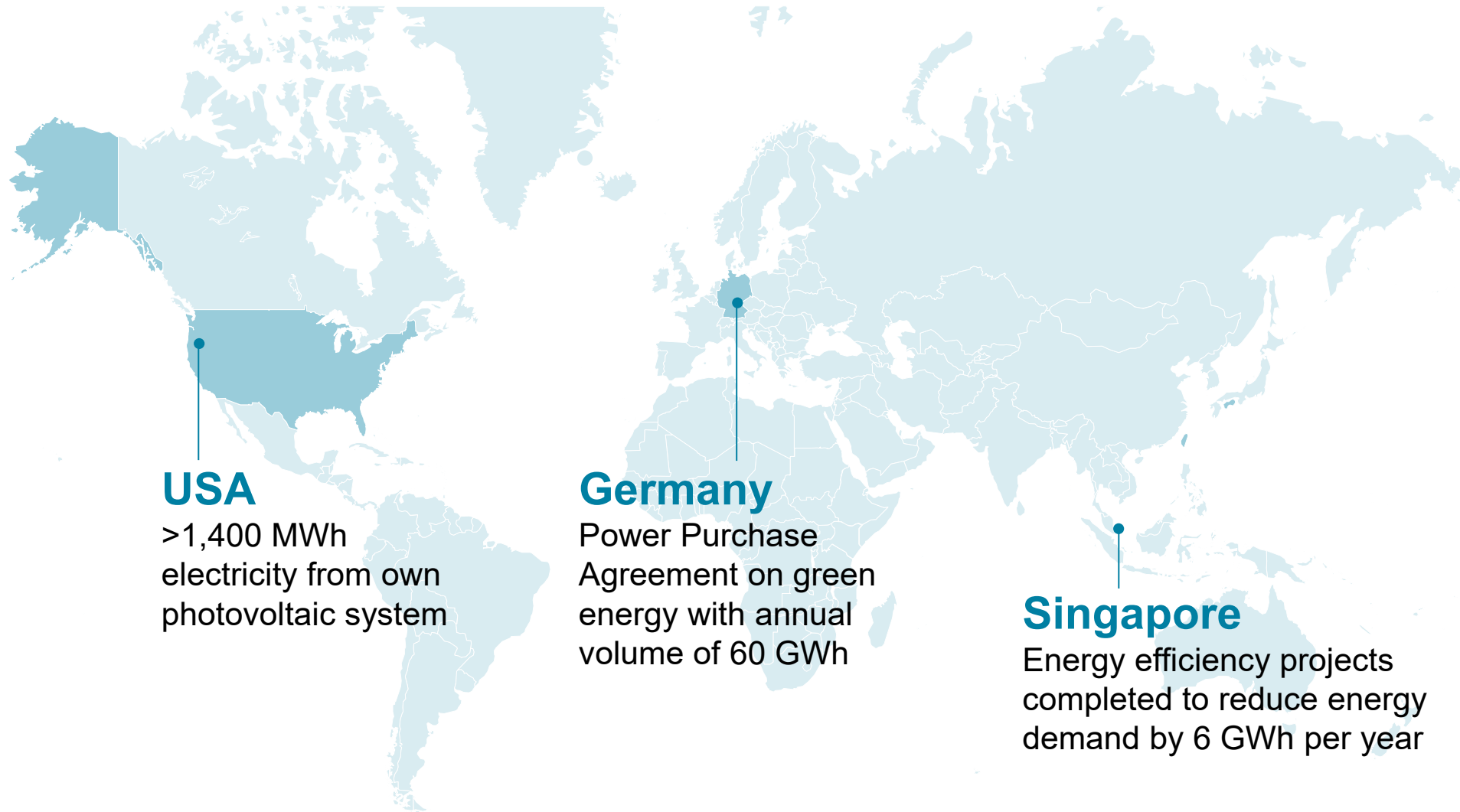
Siltronic achieved an ‘**A** – Leadership’ score in **CDP**
Climate change and Water security ratings.



With our membership at **RE100**, we are dedicated
to make our contribution to global decarbonization.

¹Absolute reduction, base year 2021; ²Net zero: GHG emissions released into the atmosphere equal the amount removed; ³Based on RE100 criteria

SUSTAINABILITY HIGHLIGHTS 2025



USA

>1,400 MWh
electricity from own
photovoltaic system

Germany

Power Purchase
Agreement on green
energy with annual
volume of 60 GWh

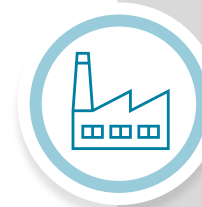
Singapore

Energy efficiency projects
completed to reduce energy
demand by 6 GWh per year



CDP A list

‘A-Leadership’ score in
CDP Climate Change
and Water Security
rating for the first time
in 2025



-31% CO₂

Reduction of absolute
CO₂ scope 1+2
emissions in 2025
compared to base
year 2021

THE SILTRONIC EXECUTIVE BOARD



Dr. Michael Heckmeier
CEO

- CEO since May 2023
(appointed until May 5, 2031)
- 25 years at Merck
- Studied physics and mathematics,
PHD in physics
- MBA, General Management



Claudia Schmitt
CFO

- CFO since July 2023
(appointed until June 30, 2031)
- 16 years at Siltronic
- 11 years at Wacker Chemie AG
- Studied Business Administration



Klaus Buchwald
COO

- COO since June 2024
(appointed until May 31, 2032)
- 21 years at Infineon
- 4 years at Rohde & Schwarz
- Studied Mechanical and Industrial
Engineering

EXECUTIVE SUMMARY

KEY TAKEAWAYS – CLEAR COMMITMENT TO PROFITABLE GROWTH AS A TECHNOLOGY LEADER



Financial Calendar and Events

September 2, 2026
**Deutsche Bank European
TMT Conference, London**

September 21, 2026
**Berenberg and Goldman
Sachs, German Corporate
Conference, Munich**

September 24, 2026
**Baader Investment
Conference, Munich**

November 23, 2026
**Deutsches
Eigentkapitalforum
Conference, Frankfurt**

November 30, 2026
**Berenberg European
Conference, London**

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