



FOUNDATION OF DIGITAL LIFE

Investor Presentation
March 2026

SILTRONIC AT A GLANCE

4,300

employees
worldwide

4

world-class
production sites

1.35

billion EUR
sales in 2025

50+

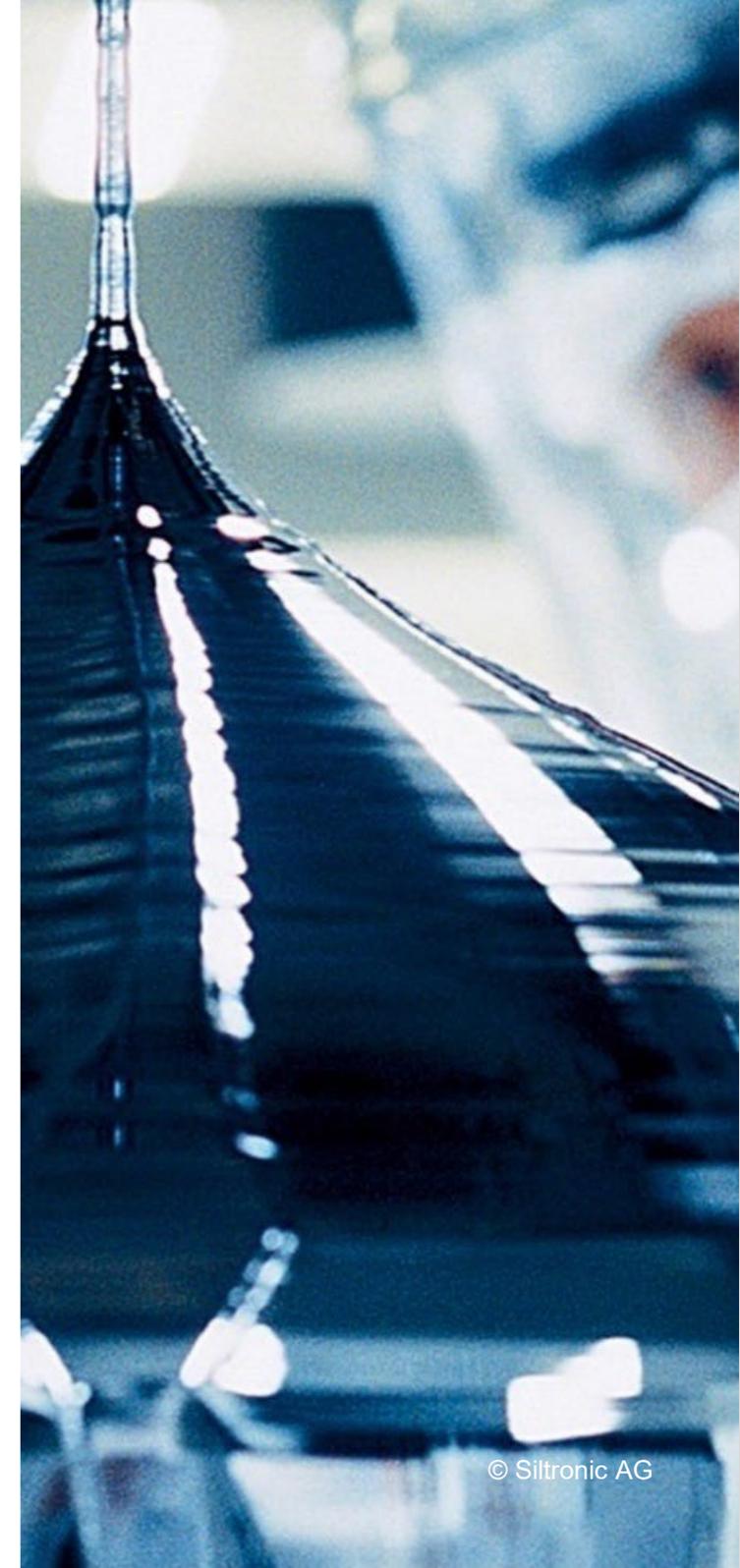
years of history
in silicon technologies

Supplier

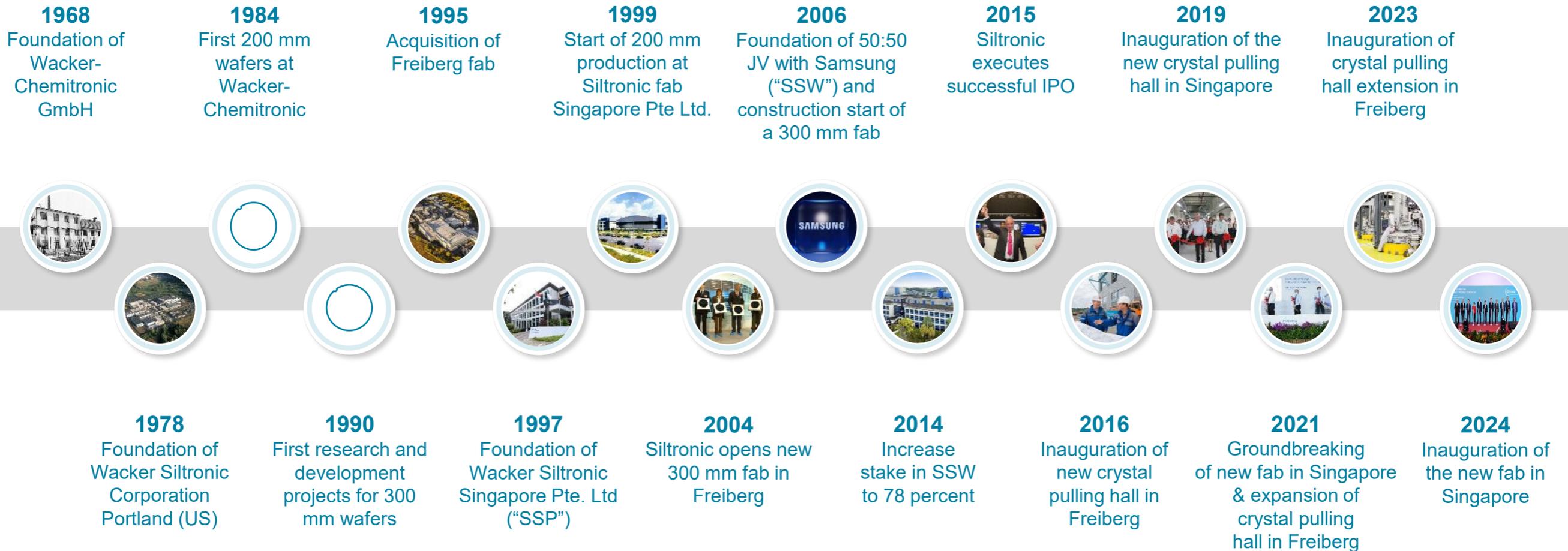
to top
semiconductor producers

23.5

percent
EBITDA margin in 2025



50+ YEARS OF HISTORY – A STORY OF SUCCESS AND INNOVATION



OUR INTERNATIONAL PRODUCTION NETWORK SERVES CUSTOMERS AROUND THE GLOBE

Singapore



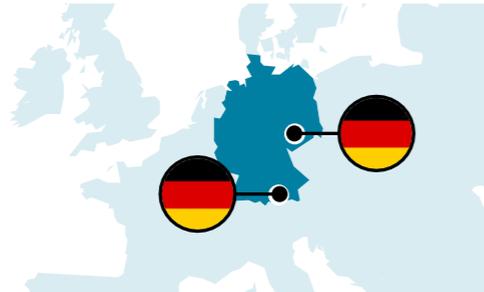
Singapore  

SSW*  CP  300 mm SSP  200 mm



300 mm
volume production in
Germany & Singapore

Germany



Germany (Burghausen, Freiberg)   

 CP  300 mm



R&D
hub in Burghausen

USA



USA (Portland / Oregon) 

 200 mm



**Leading
Edge**
production

*JV with Samsung Electronics

JV = Joint Venture
CP = Crystal Pulling
R&D = Research and Development

THE WAFER SUPPLIERS ARE FUELING THE WHOLE ELECTRONICS VALUE CHAIN

Electronics value chain (figures for 2025)

Silicon for electronic applications
USD 1.4 bn



Wacker Chemie, Hemlock, OCI, Tokuyama, Mitsubishi

5

major suppliers

Semiconductor silicon wafers
USD 11.4 bn



5

major suppliers

Semiconductor device makers
USD 786 bn



With fabs
Samsung, Infineon, TSMC, Intel, Micron, SK hynix...



Design only
Nvidia, AMD, Qualcomm...

>1,300 fabs

and roughly **50** larger chip designers

End markets
USD >5,000 bn



Smartphones
Apple, Samsung...



Servers / AI
Microsoft, Google, aws, meta...



PCs
Lenovo, HP, Dell...



Automotive
Toyota, VW, BMW, Tesla...



Industrials
Siemens, GE...



Others e.g.
White goods, Infrastructure, Gaming...

>10,000
companies

Source: TechInsights & other companies, WSTS (Silicon based), SEMI SMG, Siltronic Marketing

SILTRONIC IS WELL POSITIONED AS THE ONLY WESTERN-BASED WAFER MANUFACTURER



Around 75%
of the market is
served by the Top 5
wafer manufacturers

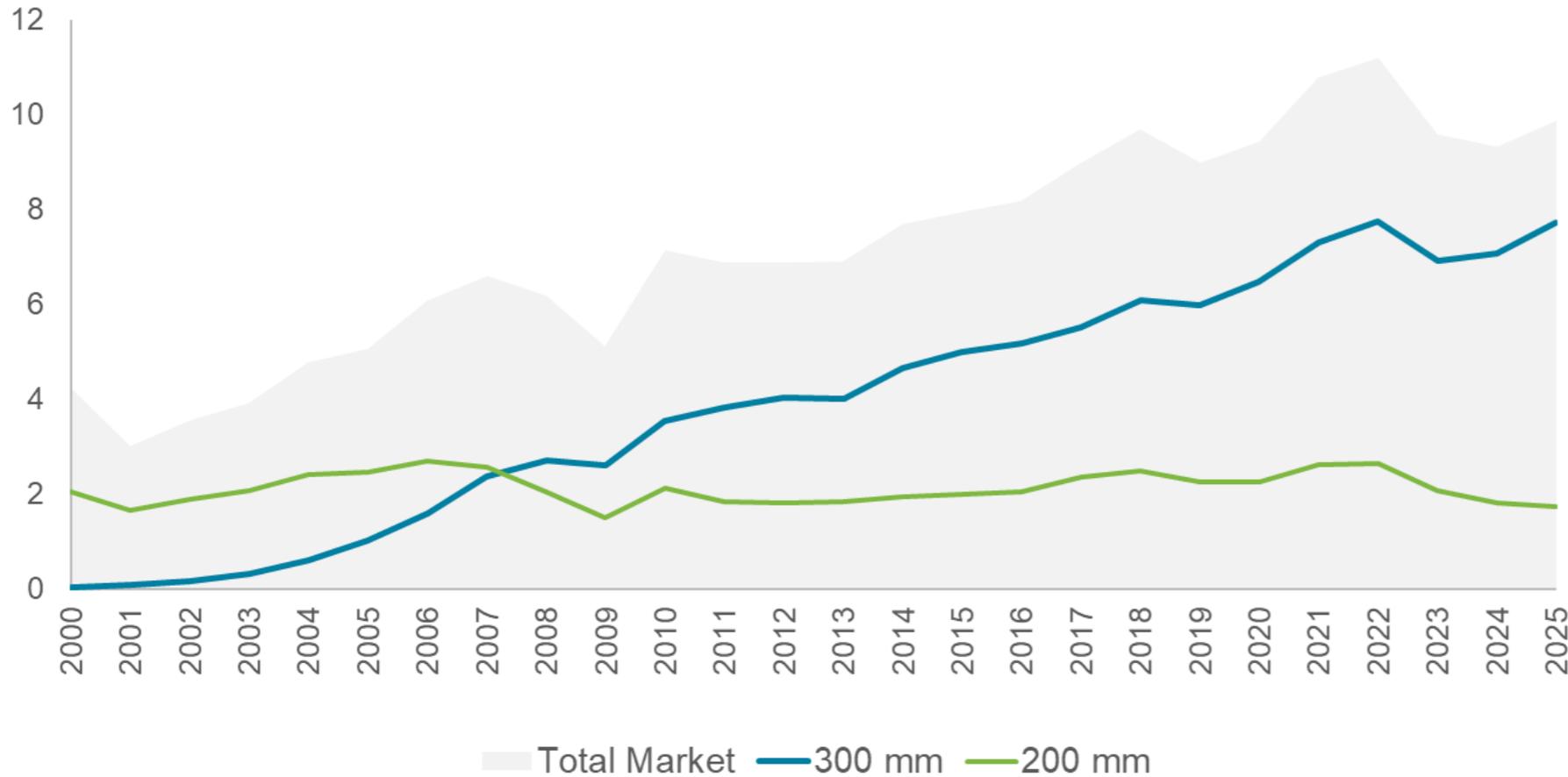


Only western
based wafer
manufacturer

Source: Q1 – Q3 2025 Quarterly revenue reports by SEMI reporting companies with Siltronic estimates on silicon wafer revenues

WAFER DEMAND GROWTH EXPECTED TO CONTINUE (CAGR¹ 5-6%), DRIVER: 300 mm WAFERS

Wafer Demand per Diameter
in mn 300 mm equivalents



CAGR 6%
for 300 mm



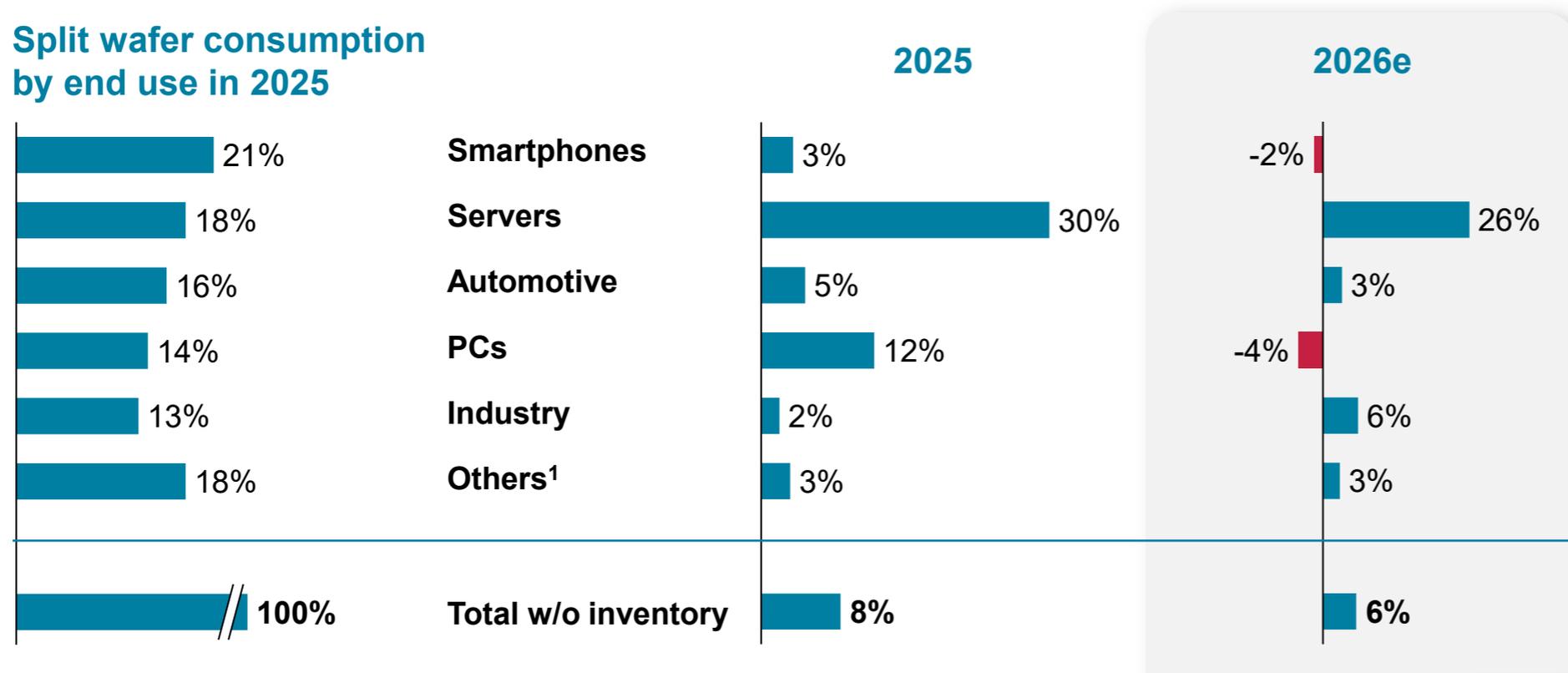
CAGR 1%
for 200 mm

Source: Siltronic estimates; ¹CAGR = Compound Annual Growth Rate (2000-2025)

2026 OUTLOOK: ~6% YOY GROWTH (PRE-INVENTORY) DRIVEN BY SERVER DEMAND

Growth in Wafer Area Consumption, in % yoy

Split wafer consumption by end use in 2025



**Inventory impact
on wafer demand**



Smartphones & PCs

softer driven by increasing Memory shortage



Cars

with reduced growth rate



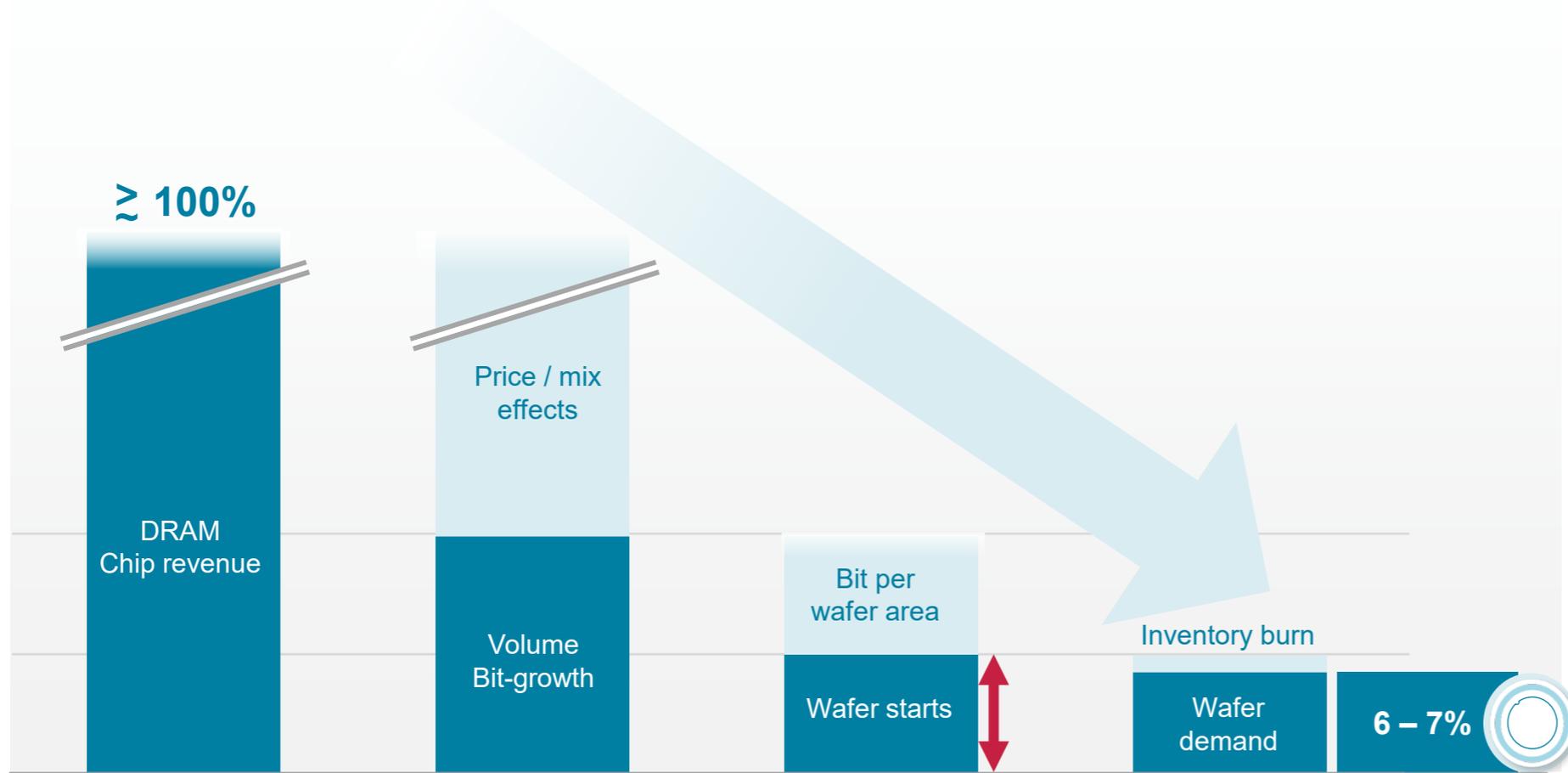
Industry

improving

¹Infrastructure Telecommunications, game consoles, consumer electronics such as televisions, white goods such as refrigerators; Source: Siltronic estimates; Chart not to scale

DRAM CHIP REVENUE CURRENTLY DE-COUPLED FROM WAFER DEMAND

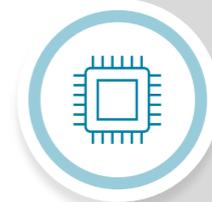
Expected DRAM Growth 2026 yoy – from sales to wafer volume



Source: Siltronic-estimates based on press statements
Chart not to scale



Supply shortage
pushes memory pricing up



Tight customer DRAM capacity
caps volume growth



~ 6-7% DRAM wafer demand
arrives at wafer industry

GRADUAL VOLUME RECOVERY WHILE HEADWINDS PERSIST

| Sales Trend yoy | 2025 act | 2026 est |
|---------------------|---------------------------|---|
| Volume | | |
| Price / Product Mix | | |
| FX | ~ EUR -45 mn ¹ | effect similar to prior-year level ² |
| TOTAL | -5 % EUR -66 mn | - mid single digit % |



Sales 2026
at PY levels, excluding SD & FX effects



Volume
recovery driven by 300 mm, 200 mm declining

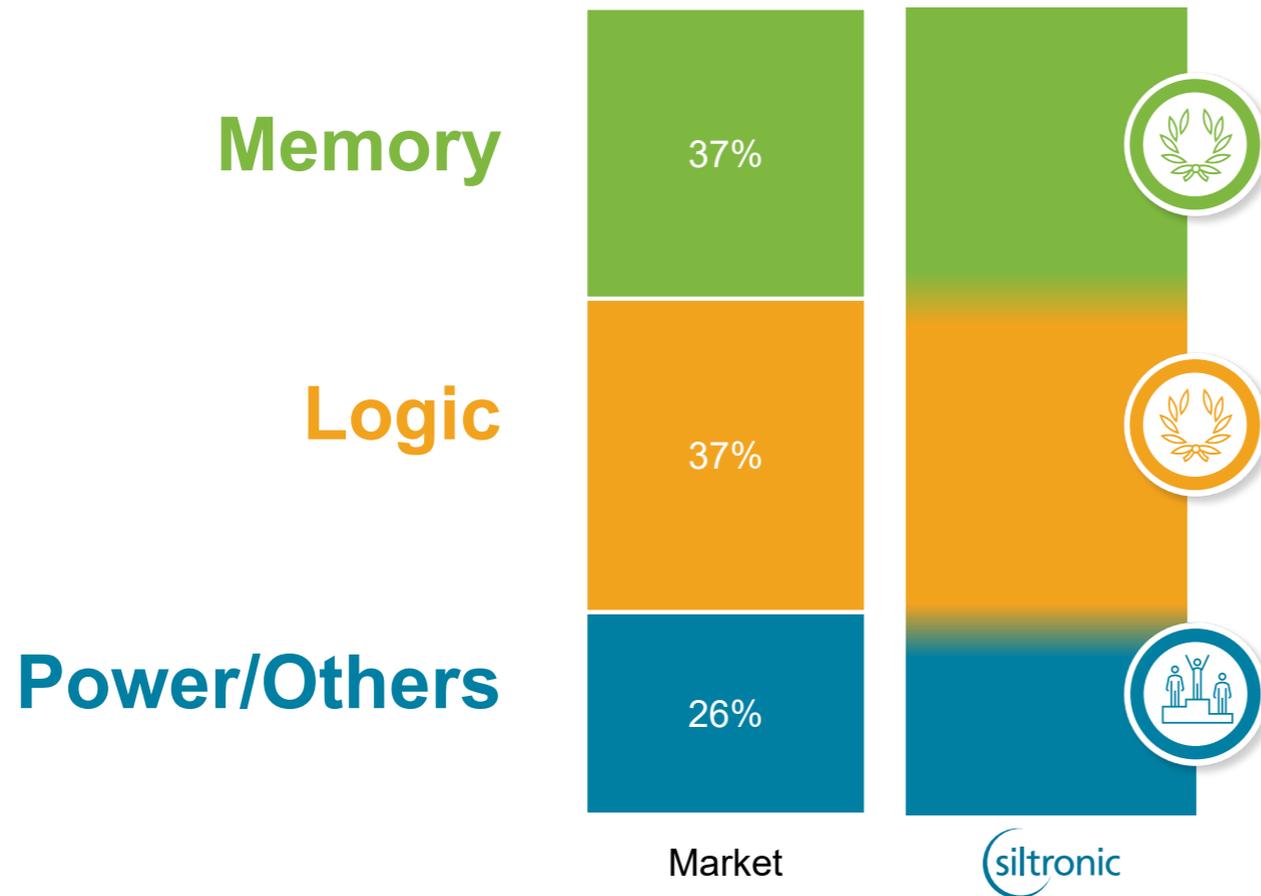


Prices
LTAs as contracted but pricing pressure outside LTAs continues

Chart not to scale, ¹EUR/USD 1.13 avg. 2025 vs. EUR/USD 1.08 2024, ²EUR/USD 1.18 assumption for 2026

SILTRONIC HAS A WELL-DIVERSIFIED AND RESILIENT PRODUCT MIX IN ALL THREE SEMI-SEGMENTS

Demand by segments in % of total demand in 2025



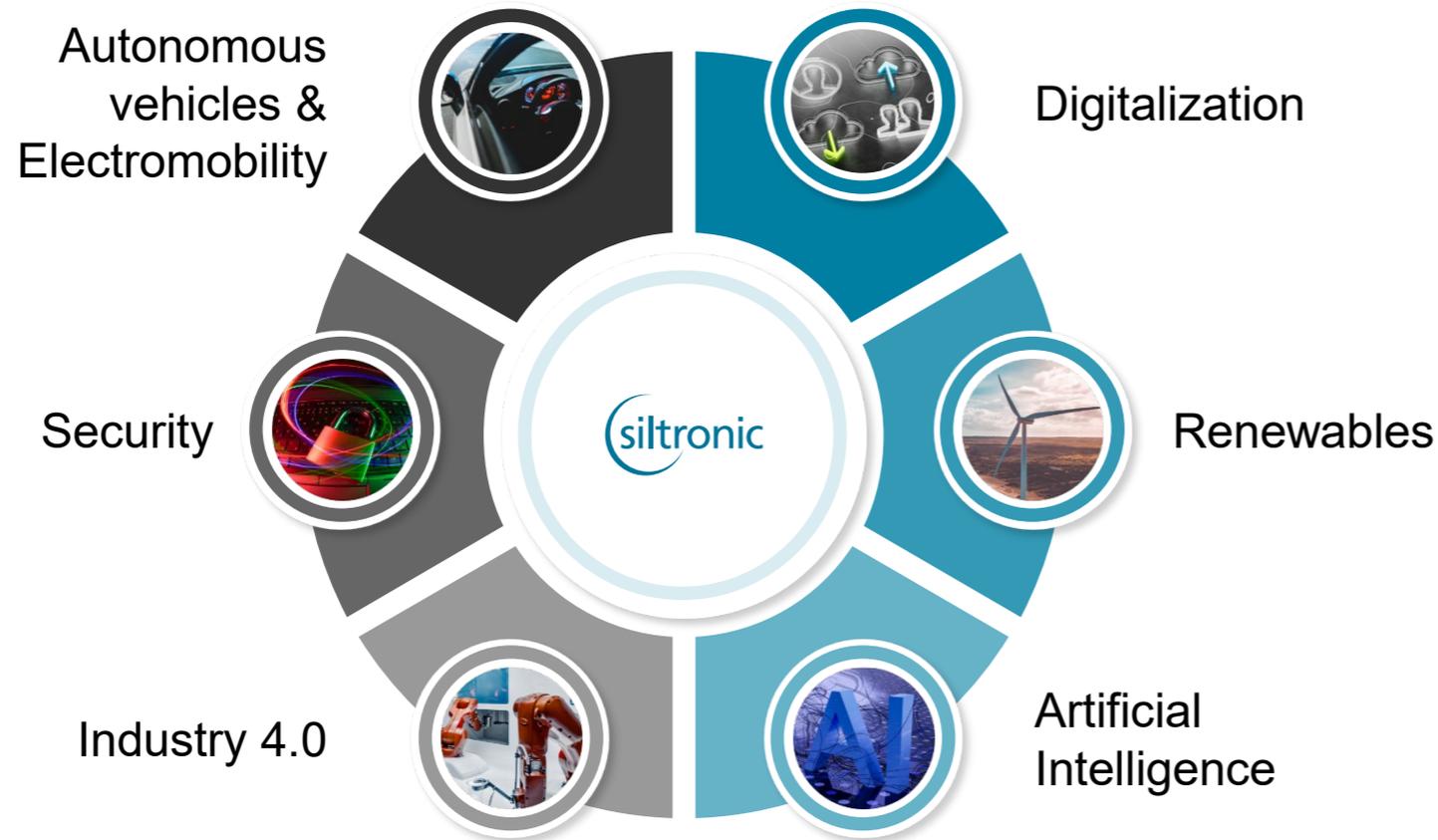
Leading Edge
Supplier



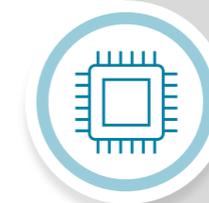
Power
Leading position

Source: Siltronic Marketing

MEGATRENDS WILL DRIVE WAFER DEMAND



Siltronic is well positioned to support each megatrend



All segments will participate and Power with over-proportional growth



Key benefits from strategic focus on Leading Edge and Power market

NEW 300 mm FAB IN SINGAPORE

Facts



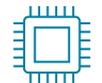
EUR 2 billion capex until 2024, from 2025 clearly reduced



Key prime customer qualifications completed in July 2025



EBITDA margin above 50%+ mid-term



First time 300 mm epi in Singapore



Up to 80% LTA share with high prepayments



2024, 2025 and 2026 with reduced ramp speed

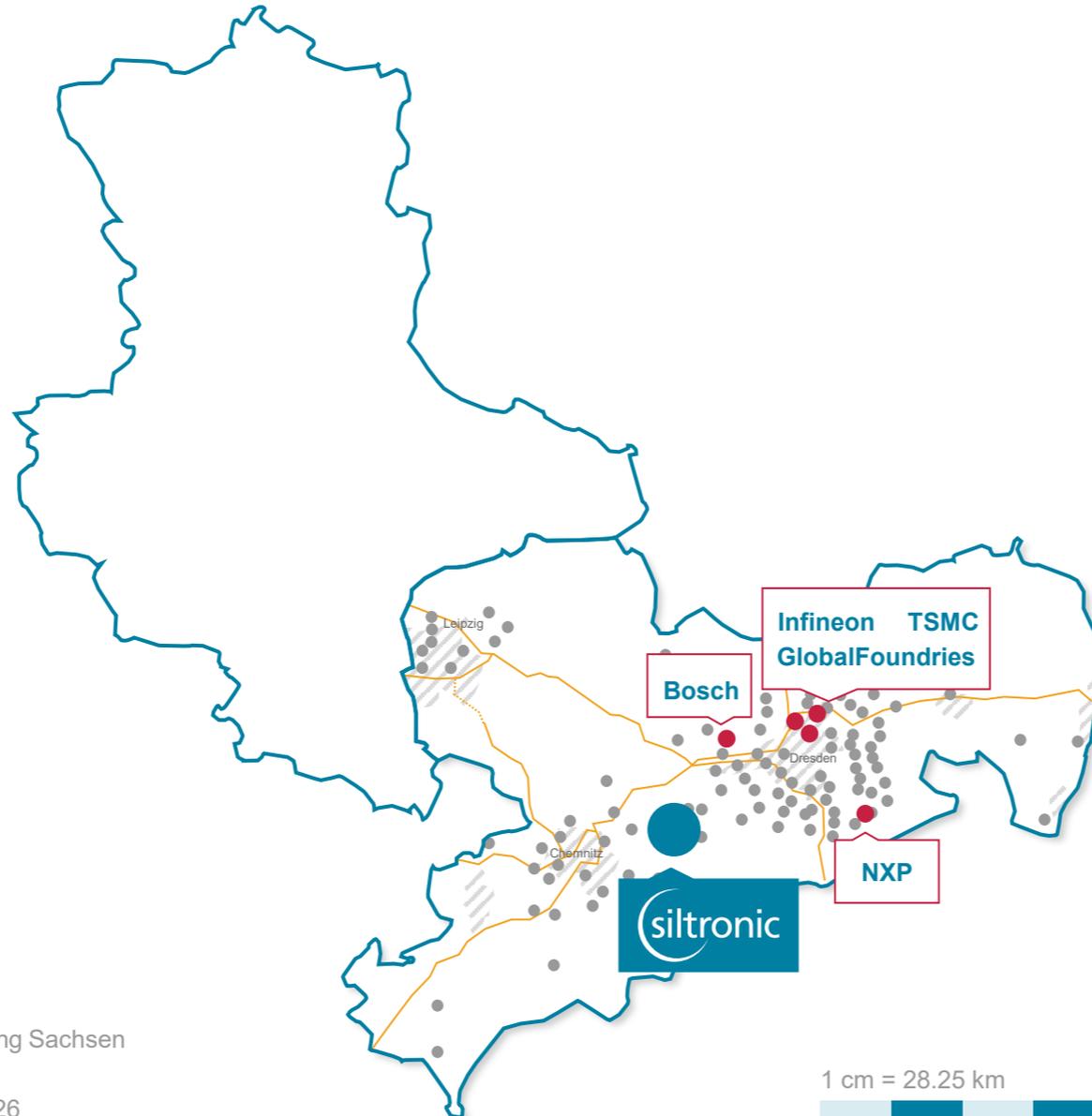


Highly automated and high economies of scale



LTA = Long Term Agreement; epi = epitaxial wafers (mostly used for the Logic industry)

IN FREIBERG WE FURTHER IMPROVED PRODUCT MIX FUELED BY INVESTMENTS



Every
2nd to 3rd
semiconductor in the
EU is from this region



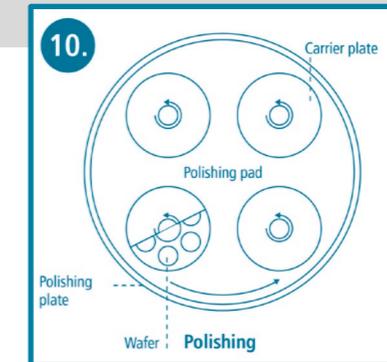
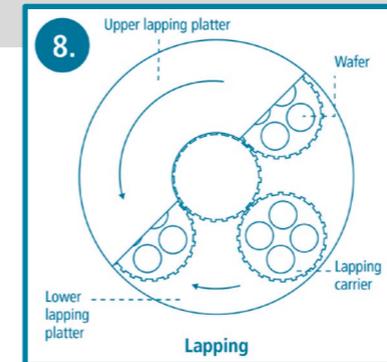
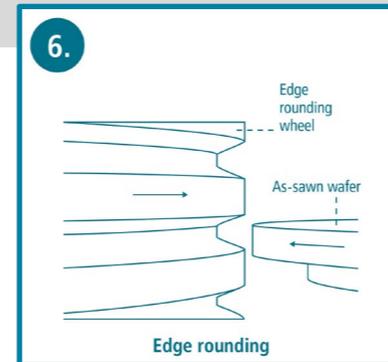
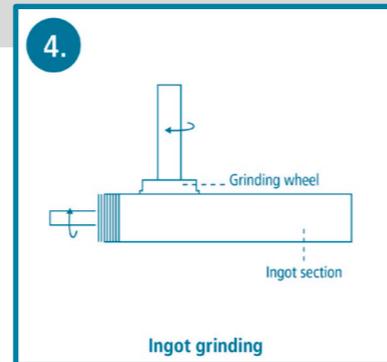
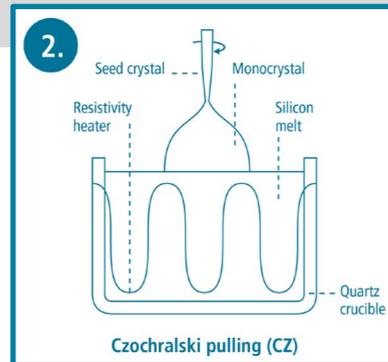
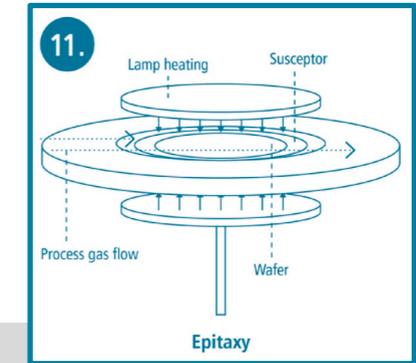
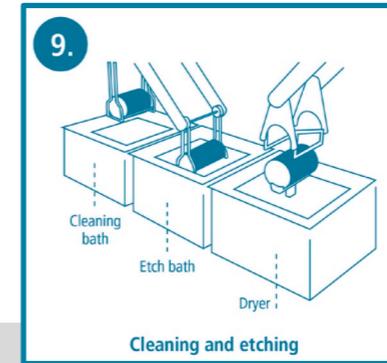
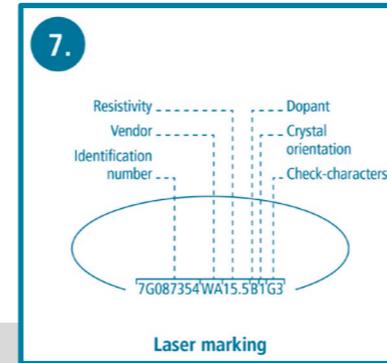
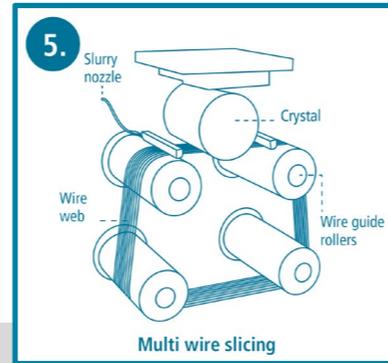
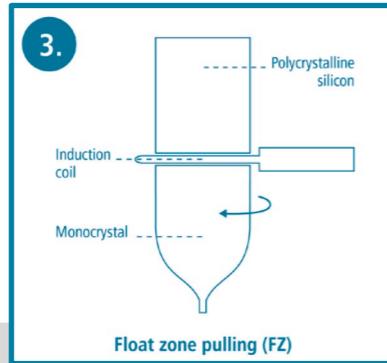
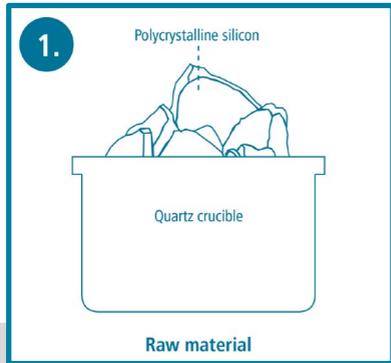
Investments
of more than EUR 1 bn
since 1995 by Siltronic



First-class
geographical position
in light of EU ambitions

Source: Siltronic; Silicon Saxony/Wirtschaftsförderung Sachsen

WAFER PRODUCTION PROCESS



STRONG FOCUS ON GROWTH DRIVERS R&D AND INNOVATION

R&D
hub in Burghausen

450
R&D employees

1,900
patents

5-6%
of sales planned
as R&D spendings



Partnering with leading innovation platforms



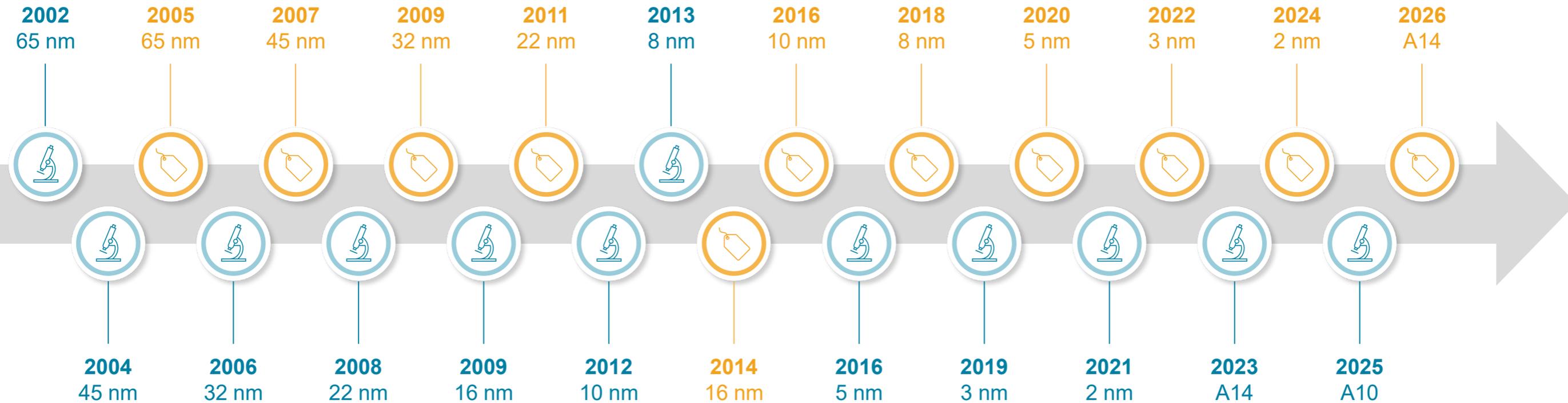
Leibniz Institute
for High
Performance
Microelectronics



R&D Innovation Strategy

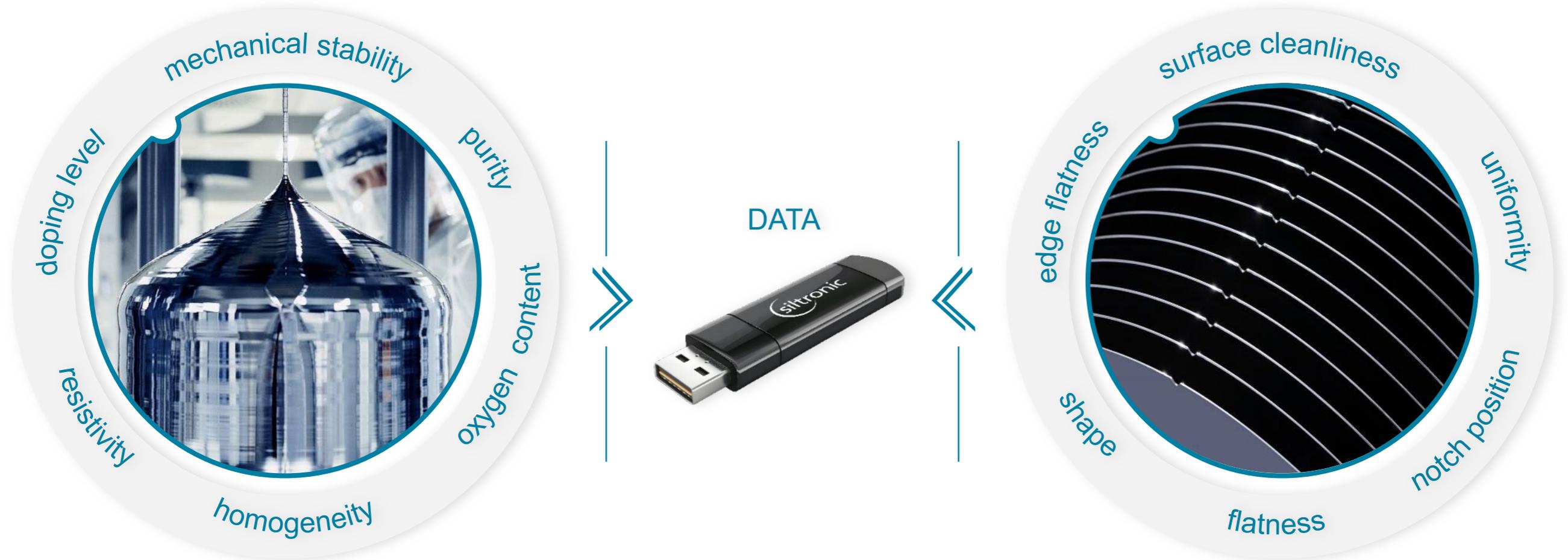
- Stay one generation ahead as a **Technology Leader**
- Focus on **Leading Edge** and **Power**
- Find new business opportunities and **expand** our product **portfolio**

SILTRONIC HAS A PROVEN TRACK RECORD AS A TECHNOLOGY LEADER



 R&D Start
  Commercialization

A NUMBER OF KEY INGOT AND WAFER PROPERTIES DEFINE THE WAFER SPECIFICATION AND ITS FURTHER USE BY THE CUSTOMER



PURITY AND FLATNESS REQUIREMENTS FOR WAFERS ARE EXTREME, AS SCALING THEM INTO THE MACRO WORLD SHOWS

Particles

Particle size specified in the nm range. Requirements are equivalent to a maximum of 10 grains of sand distributed over the city of Munich.



nm = nanometer

Flatness

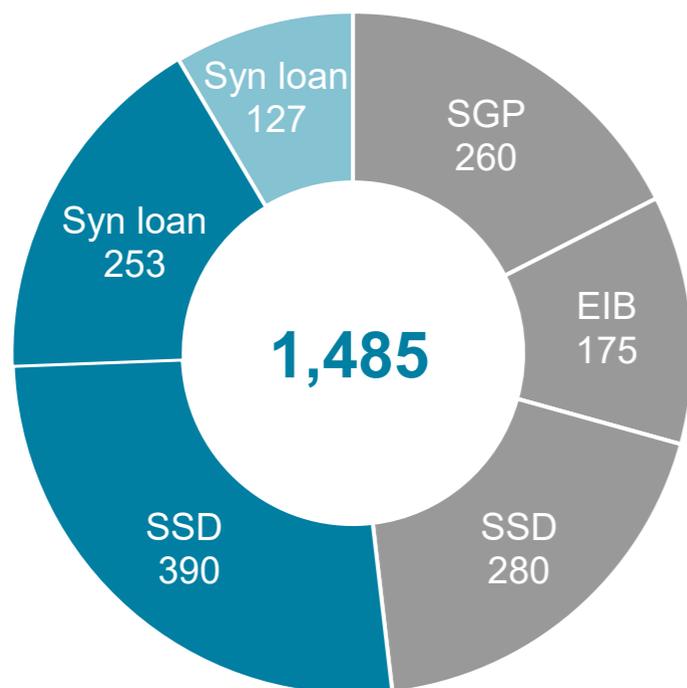
20 nm on a wafer are equivalent to the height of a leaf on the surface of Lake Chiemsee – or a single bacteria on a tennis court.



OUTLOOK

DEBT REPAYMENTS STARTED IN 2025

Debt Financing Instruments in EUR million



- Fixed loans, drawn
- Variable loans, drawn
- Syn loan, not drawn yet

Figures rounded

SSD = Schuldscheindarlehen (Promissory Note Loan)

Maturity Profile of Debt Financing in EUR million

| 2026 | 2027 | 2028 | Σ 2029 – 2033 |
|------|------|------|-------------------------|
| 100 | 250 | 220 | 790 |



Prepayments
of EUR 10 mn expected
to be refunded in 2026

Liquidity in EUR million

| December 31, 2025 | Syn loan (not drawn) |
|----------------------|-------------------------|
| 531 | 127 |



Interest
expenses in the
ballpark of EUR 50 mn
expected in 2026

CONTINUING TO FOCUS ON CAPEX, COSTS AND CASH

Strict Capex Discipline



Deferred capacity spend, restrictive project approvals

Cost Program



Full-scope cost management, tackling all cost drivers

Other Cash Measures



Particularly comprehensive working capital management

Major cost items 2026



Depreciation



Labor



Supplies



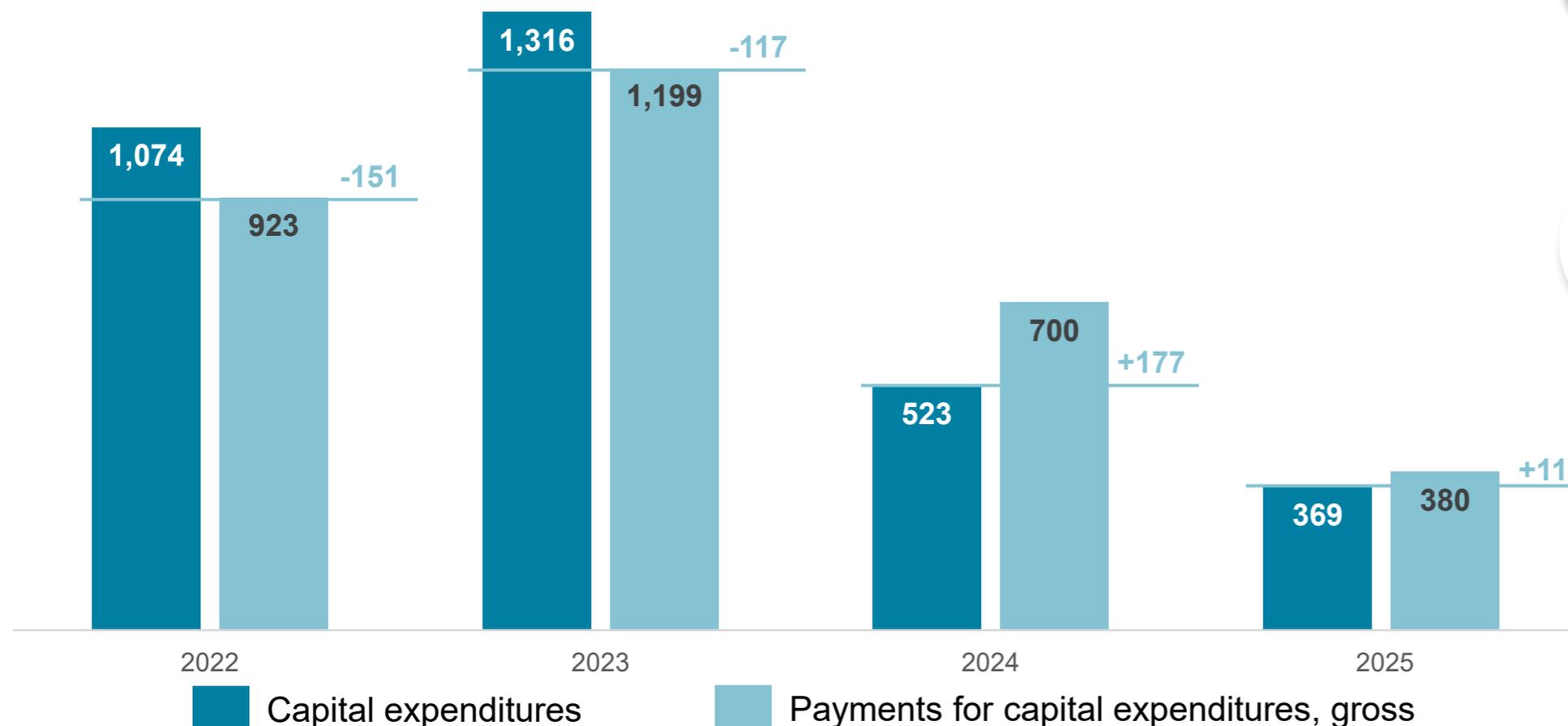
Polysilicon



Energy

CAPEX SIGNIFICANTLY DOWN, PAYMENTS FOR CAPEX ON INVEST-LEVEL

Capex and Payments for Capex, in EUR million



Trade payables

related to capex remain elevated at YE 25



Payment

roll-over effects to 2026 expected

SILTRONIC'S TECHNOLOGICAL LEADERSHIP AND EXCELLENCE RECOGNIZED WITH MULTIPLE AWARDS IN THE LAST 12 MONTHS



EPIC Supplier Award

Intel, April 2025



BEST in Value Award

Samsung, September 2025



Best Supplier Award

SSMC, September 2025



Outstanding Supplier Performance Award

Micron, November 2025



Best Silicon Supplier Award

ST, November 2025



2026 GUIDANCE REFLECTS HEADWINDS

Sales

Mid-single digit below 2025
EUR/USD 1.18

EBITDA margin

Between 20% and 24%

Depreciation

Between EUR 490 and 520 mn

EBIT

Significantly below the previous year

Capex

Between EUR 180 and 220 mn

Net Cash Flow

In the range of the previous year



ESG @ SILTRONIC

Clear commitment to an emission-free future



SILTRONIC IS ENABLING TECHNOLOGIES FOR A SUSTAINABLE FUTURE

Power focus

Power chips are crucial for decarbonization and enable renewable energies and EV's



Focus on **Power wafers**
+
R&D focus in **special products**
and **new materials**



Leading Edge focus

New chip generations increase energy efficiency and need less power



Leading Edge wafers
+
R&D focus on **new Design Rules**



Strategy

on Leading Edge and Power wafers supports sustainability strategy



Investments

needed to keep our equipment state of the art for Leading Edge wafers



R&D focus

is crucial for sustainability strategy

AMBITIOUS TARGETS AND GLOBAL COMMITMENTS KEEP US GOING



Climate – Scope 1+2 CO₂ emissions

2030: -42%¹
2045: Net zero²
Status 2025: -31%¹



Energy – renewable

2030: 60% share
2045: 100% share
Status 2025: 26% share³



Siltronic supports the 10 principles of the **UN Global Compact**.

We contribute to the sustainable development goals of the **United Nations**.

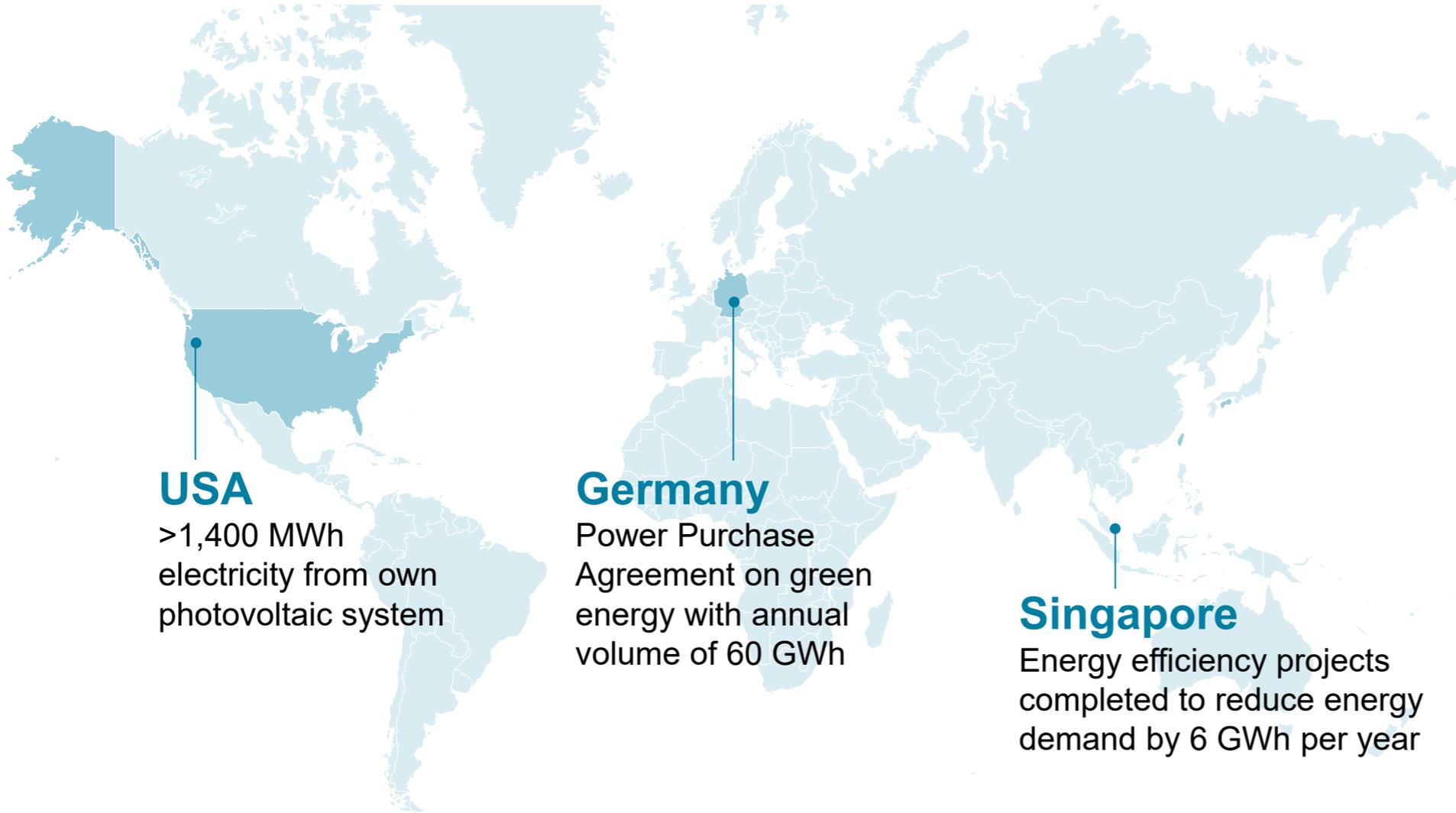
As a supplier to the electronics industry, Siltronic is a “Regular” member of the **Responsible Business Alliance (RBA)**.

Siltronic achieved an ‘**A** – Leadership’ score in **CDP** Climate change and Water security ratings.

With our membership at **RE100**, we are dedicated to make our contribution to global decarbonization.

¹Absolute reduction, base year 2021; ²Net zero: GHG emissions released into the atmosphere equal the amount removed; ³Based on RE100 criteria

SUSTAINABILITY HIGHLIGHTS 2025



USA

>1,400 MWh electricity from own photovoltaic system

Germany

Power Purchase Agreement on green energy with annual volume of 60 GWh

Singapore

Energy efficiency projects completed to reduce energy demand by 6 GWh per year



CDP A list

'A-Leadership' score in CDP Climate Change and Water Security rating for the first time in 2025



-31% CO₂

Reduction of absolute CO₂ emissions scope 1+2 in 2025 compared to base year 2021

THE SILTRONIC EXECUTIVE BOARD



Dr. Michael Heckmeier
CEO

- CEO since May 2023
(appointed until May 5, 2031)
- 25 years at Merck
- Studied physics and mathematics,
PHD in physics
- MBA, General Management



Claudia Schmitt
CFO

- CFO since July 2023
(appointed until June 30, 2031)
- 16 years at Siltronic
- 11 years at Wacker Chemie AG
- Studied Business Administration



Klaus Buchwald
COO

- COO since June 2024
(appointed until May 31, 2027)
- 21 years at Infineon
- 4 years at Rohde & Schwarz
- Studied Mechanical and Industrial
Engineering

EXECUTIVE SUMMARY

KEY TAKEAWAYS – CLEAR COMMITMENT TO PROFITABLE GROWTH AS A TECHNOLOGY LEADER



Financial Calendar and Events

| | | | | |
|--|------------------------|-------------|---|--|
| Mar 25, 2026 | Apr 29, 2026 | May 8, 2026 | May 28, 2026 | September 2/3, 2026 |
| Jefferies Pan Euro Mid-Cap Conference, London | Q1 2026 results | AGM | Deutsche Bank European Champions Conference, Frankfurt | Deutsche Bank European TMT Conference, London |

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