

SET TO GROW

Investor Presentation July 2025

SILTRONIC AT A GLANCE

4,400

employees worldwide

4

world-class production sites

1.4

billion EUR sales in 2024

50+

years of history in silicon technologies

Supplier

to top

semiconductor producers

26

percent EBITDA margin in 2024



50+ YEARS OF HISTORY – A STORY OF SUCCESS AND INNOVATION

1968 Foundation of Wacker-Chemitronic **GmbH**

1984 First 200 mm wafers at Wacker-Chemitronic

1995 Acquisition of Freiberg fab

1999 Start of 200 mm production at Siltronic fab Singapore Pte Ltd.

2006 Foundation of 50:50 JV with Samsung ("SSW") and construction start of a 300 mm fab

2015 Siltronic executes successful IPO

2019 Inauguration of the new crystal pulling hall in Singapore

2023 Inauguration of crystal pulling hall extension in Freiberg





















1978 Foundation of Wacker Siltronic Corporation Portland (US)

1990 First research and development projects for 300 mm wafers

1997 Foundation of Wacker Siltronic Singapore Pte. Ltd ("SSP")

2004 Siltronic opens new 300 mm fab in Freiberg

2014 Increase stake in SSW to 78 percent

2016 Inauguration of new crystal pulling hall in Freiberg

2021 Groundbreaking of FabNext in Singapore & expansion of crystal pulling hall in Freiberg

2024 Inauguration of the new fab in Singapore

OUR INTERNATIONAL PRODUCTION NETWORK SERVES CUSTOMERS AROUND THE GLOBE

Singapore







volume production in Germany & Singapore

Germany







R&D hub in Burghausen

USA



USA (Portland / Oregon) 200 mm



Leading Edgeproduction

*JV with Samsung Electronics

JV = Joint Venture
CP = Crystal Pulling
R&D = Research and Development

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THE WAFER SUPPLIERS ARE FUELING THE WHOLE ELECTRONICS VALUE CHAIN

silicon for electronic applications
USD 1.3 bn

Semiconductor silicon wafers
USD 11.5 bn

Semiconductors device makers
USD 621 bn

End markets
USD >5,000 bn











>1,300 fabs and roughly 50 larger chip designers



Smartphones
Apple,
Samsung...



Servers / Al Microsoft, Google, aws, meta...



PCs Lenovo, HP, Dell...



Automotive Toyota, VW, BMW, Tesla...



Industrials Siemens, GE...



Others e.g.
White goods,
Infrastructure,
Gaming...

>10,000 companies

Source: TechInsights & other companies, WSTS (Silicon based), SEMI SMG, Siltronic Marketing

major suppliers

SILTRONIC IS WELL POSITIONED AS THE ONLY WESTERN-BASED WAFER MANUFACTURER





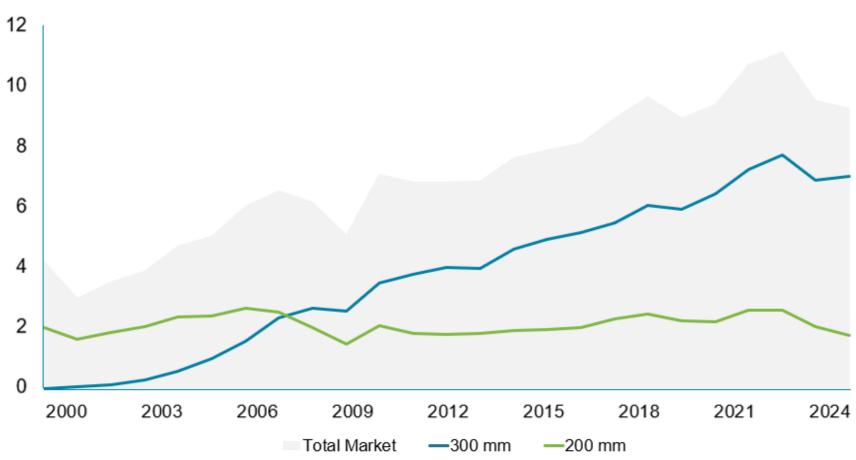


Source: Q1-Q3 2024 Quarterly revenue reports by SEMI reporting companies with Siltronic estimates on silicon wafer revenues

WAFER DEMAND GROWTH EXPECTED TO CONTINUE (CAGR 4-5%), DRIVER 300 mm WAFERS

Wafer Demand per Diameter

in mn 300 mm equivalents





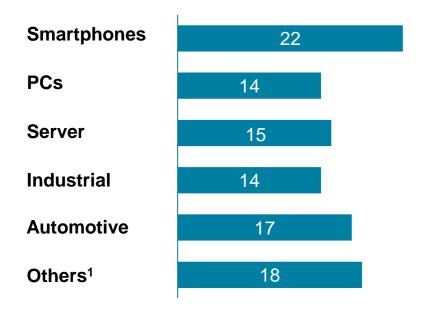


Source: Siltronic estimates

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END MARKETS GROWING BUT TARIFF UNCERTAINTIES PERSISTS

Consumption of wafers by end use 2024, in %





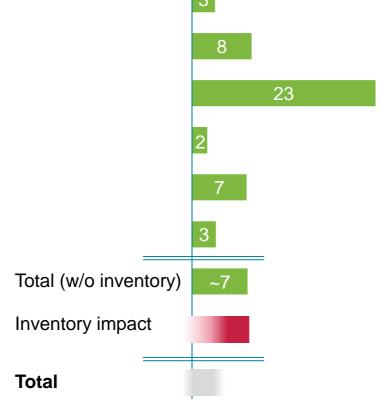


PCs

Driven by Windows 11 launch



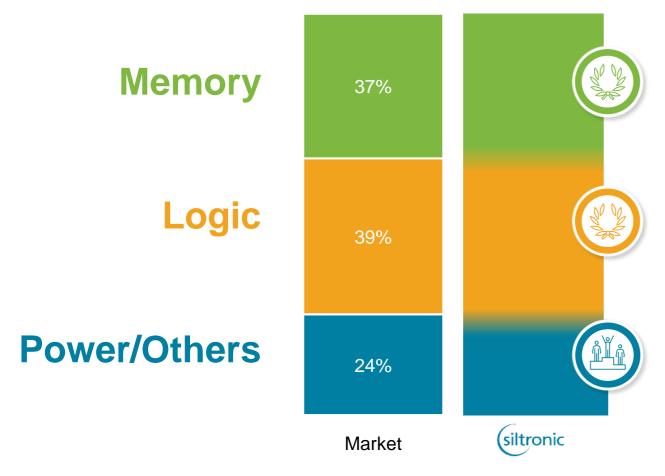
Smartphones Softer



Source: Siltronic estimates, 1 Infrastructure Telecommunications, game consoles, consumer electronics such as televisions, white goods such as refrigerators; Chart not to scale

SILTRONIC HAS A WELL-DIVERSIFIED AND RESILIENT PRODUCT MIX IN ALL THREE SEMI-SEGMENTS

Demand by segments in % of total demand in 2024



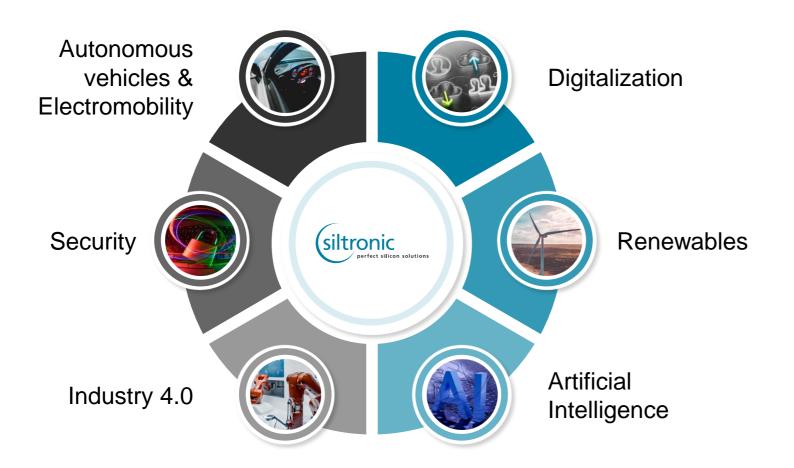




Source: Siltronic Marketing

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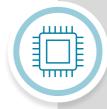
MEGATRENDS WILL DRIVE WAFER DEMAND





Siltronic

is well positioned to support each megatrend



All segments

will participate and Power with over proportional growth



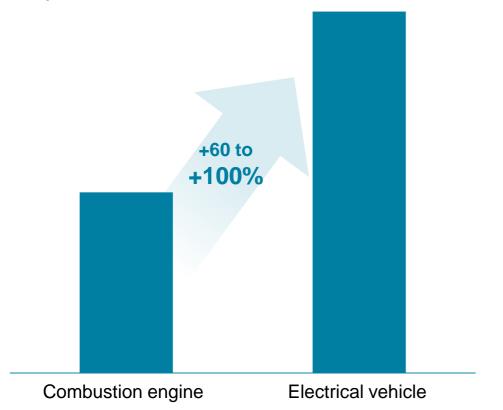
Key benefits

from strategic focus on Leading Edge and Power market

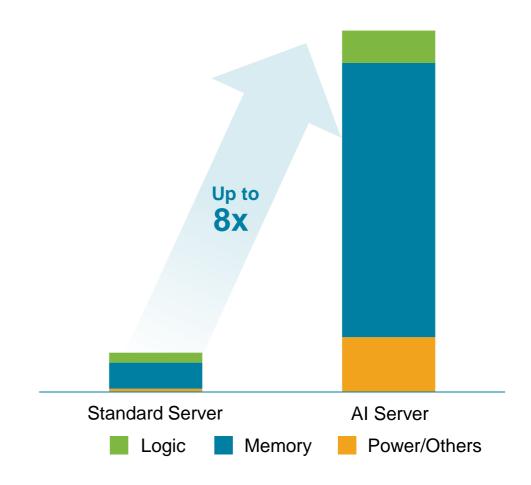
FOR EXAMPLE: ELECTROMOBILITY AND AI ARE PUSHING WAFER **DEMAND**

Silicon content between combustion engine and electrical vehicle

in cm² per car



Silicon content between standard server vs. Al server in cm² per server



Source: BS Sept. 2023 Global I/O Semiconductors; UBS May 2023; Omdia Q4 2022; Siltronic Marketing estimates

FLAGSHIP PROJECT: INAUGURATION OF OUR NEW 300 mm FAB IN SINGAPORE TOOK PLACE IN JUNE

Facts



EUR 2 billion capex until 2024, in 2025 clearly reduced



Key prime customer qualifications completed



Group EBITDA margin above 50%+ mid-term



First time 300 mm epi in Singapore



Up to 80% LTA share with high prepayments



2024 and 2025 with reduced ramp speed



Highly automated and high economies of scale



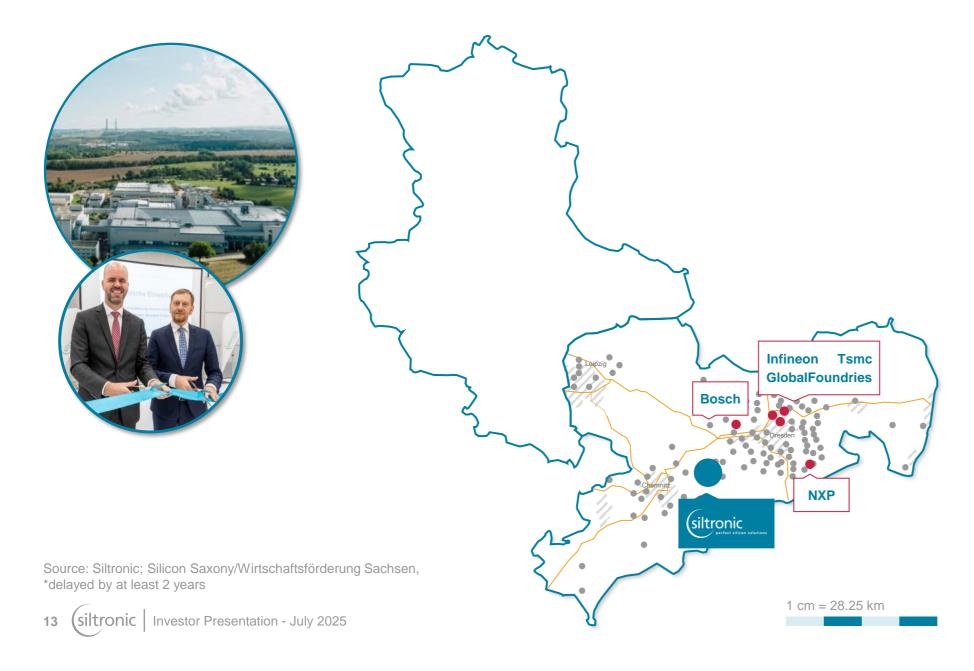






LTA = Long Term Agreement; epi = epitaxial wafers (mostly used for the logic industry)

IN FREIBERG WE FURTHER IMPROVED PRODUCT MIX FUELED BY INVESTMENTS





Every 3rd to 2ndsemiconductor in the EU is from this region



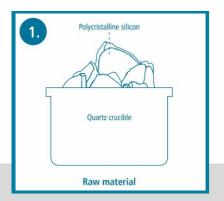
Investments of more than EUR 1 bn since 1995 by Siltronic

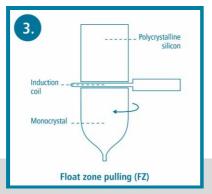


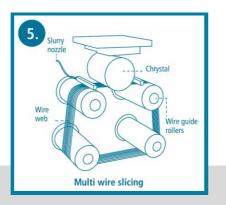
First-class

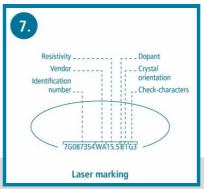
geographical position in light of EU ambitions

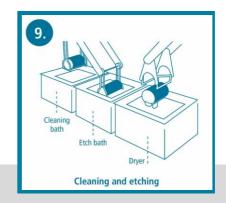
WAFER PRODUCTION PROCESS

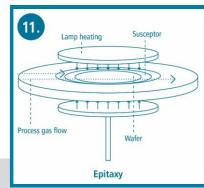


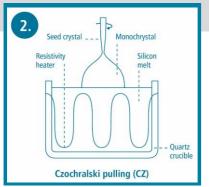


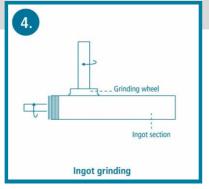


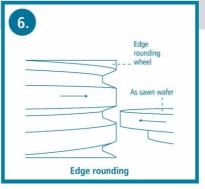


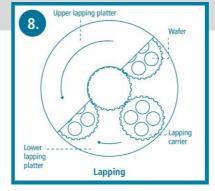


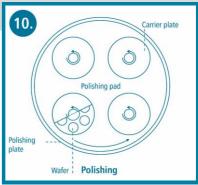












STRONG FOCUS ON GROWTH DRIVERS R&D AND **INNOVATION**

R&D

hub in Burghausen



Partnering with leading innovation platforms













R&D employees

















1,900 patents



4-5% of sales planned as R&D spendings



R&D Innovation Strategy

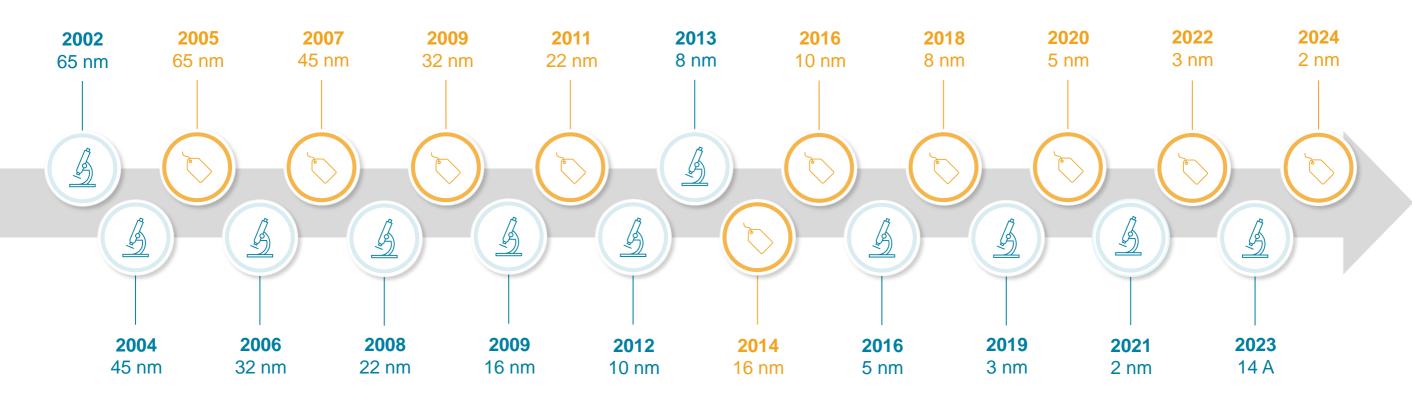
Stay one generation ahead as a Technology Leader

Focus on Leading Edge and Power

Find new business opportunities and expand our product portfolio

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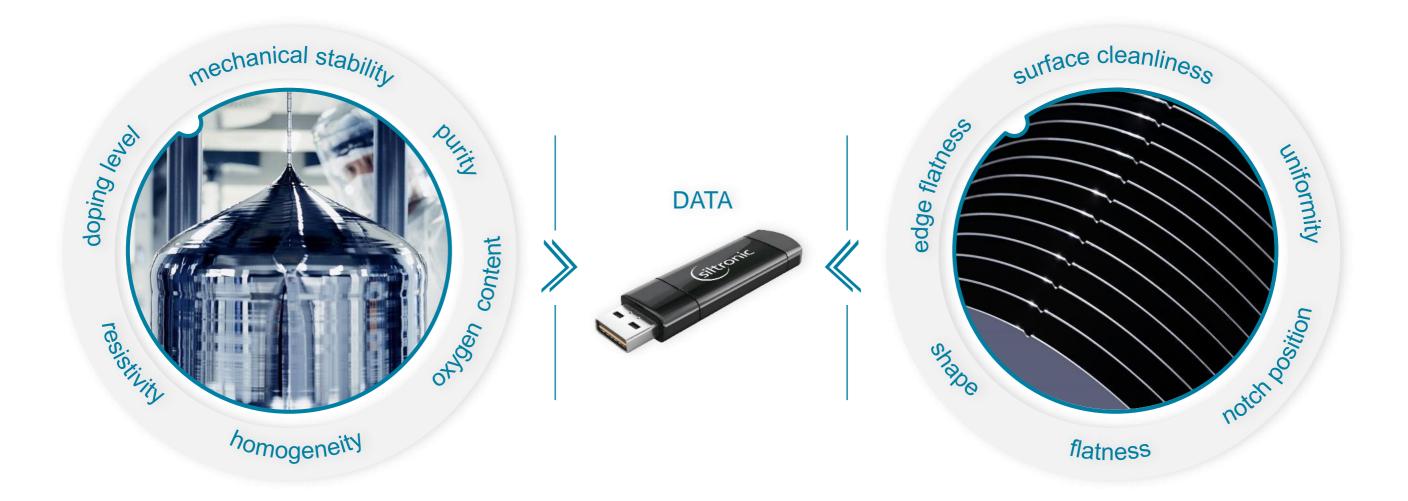
SILTRONIC HAS A PROVEN TRACK RECORD AS A TECHNOLOGY **LEADER**







A NUMBER OF KEY INGOT AND WAFER PROPERTIES DEFINE THE WAFER SPECIFICATION AND ITS FURTHER USE BY THE CUSTOMER



PURITY AND FLATNESS REQUIREMENTS FOR WAFERS ARE EXTREME, AS SCALING THEM INTO THE MACRO WORLD SHOWS

Particles

Particle size specified in the nm range. Requirements are equivalent to a maximum of 10 grains of sand distributed over the city of Munich.



Flatness

20 nm on a wafer are equivalent to the height of a leaf on the surface of Lake Chiemsee - or a single bacteria on a tennis court.



nm = nanometer

OUTLOOK AND FINANCIAL STRENGTH

Increased risks due to indirect tariff and FX risks



SALES GUIDANCE ADJUSTED TO NEW FX REALITY

Sales

mid-single digit below 2024 (EUR/USD: 1.15 for H2)

(Previous: in the region of 2024¹)

EBITDA Margin

Between 21% and 25%

Depreciation

Between EUR 340 and 400 mn (Previous: EUR 380 to 440 mn)

EBIT

Significant decline

Capex

Between EUR 350 and 400 mn

Net cash flow

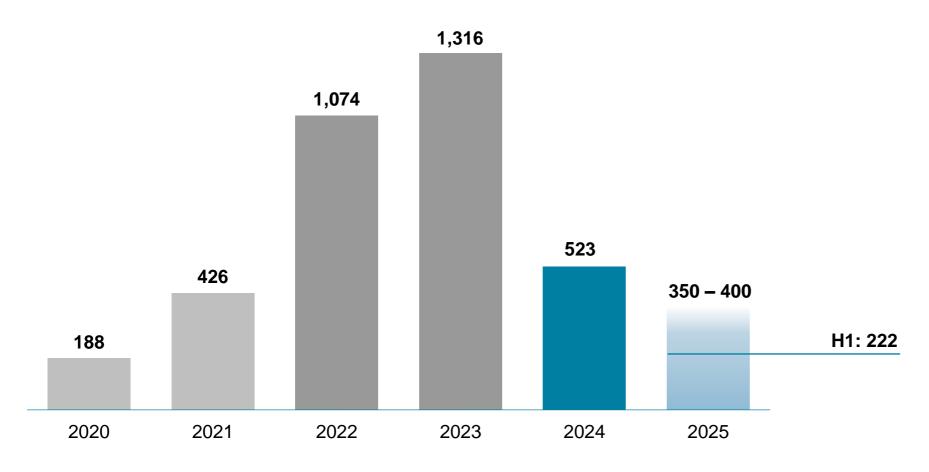
Significantly improved compared to previous year but still notably negative



¹ EUR/USD: 1.08

SIGNIFICANT CAPEX REDUCTION IN H1 2025

Capex, in EUR million





Capex 2025 Ongoing focus on new



Capex CF Payments 2025 clearly

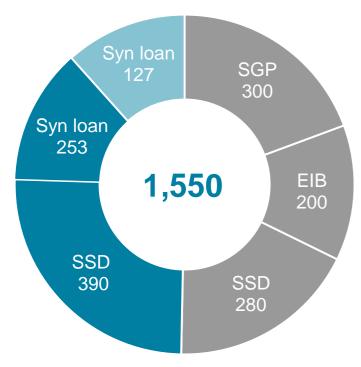
exceeding invest level

Chart not to scale

SMALL PORTION OF SYN LOAN DRAWN IN Q2 2025

Debt financing instruments

in EUR million



Fixed loans, drawn

Variable loans, drawn

Syn loan, not drawn yet

Figures rounded

SSD=Schuldscheindarlehen (Promissory Note Loan)

Maturity profile of debt financing in EUR million

| 2025 | 2026 | 2027 | ∑ 2028 - 2032 |
|------|------|------|------------------|
| 65 | 105 | 250 | 1.130 |



Prepayments

of EUR 42 mn expected to be refunded in the next 12 month







Interest

expenses in the ballpark of EUR 50 mn expected in 2025

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NAVIGATING SOFT DEMAND BY COST EFFICIENCY AND CASH PROGRAM

Cash and Cost reduction measures



Production

Focus on productivity and efficiency



Capex

Clearly reduced due to progress of new Fab and strict capex control



Labor

Direct headcount adjusted to reduced output, indirect headcount program



Working Capital

Close tracking and effective management



Costs in general

Intensified activities to optimize our cost position



Dividend

Reduced amount of EUR 0.20 per share for 2024

Major cost items 2025



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ESG @ SILTRONIC

Clear commitment to an emission-free future



SILTRONIC IS ENABLING TECHNOLOGIES FOR A SUSTAINABLE FUTURE

Power focus

siltronic

Power chips are crucial for decarbonization and enable renewable energies and EV's





Focus on **Power** wafers

R&D focus in special products and new materials



New chip generations increase energy efficiency and need less power





Leading Edge wafers

R&D focus **on new Design Rules**



Strategy

on leading edge and power wafers supports sustainability strategy



Investments

needed to keep our equipment state of the art for leading edge wafers



R&D focus

is crucial for sustainability strategy



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AMBITIOUS TARGETS AND GLOBAL COMMITMENTS KEEP US GOING



Climate - Scope 1+2

CO₂ emissions

2030: -42%¹

2045: Net zero²

Status 2024: -27%



Energy – renewable

2030: 60% share

2045: 100% share

Status 2024: 19% share³











Siltronic supports the 10 principles of the UN Global Compact.

We contribute to the sustainable development goals of the UN.

As a supplier to the electronics industry, Siltronic is a "Regular" member of the Responsible Business Alliance (RBA).

Siltronic participates in CDP Climate change and Water security evaluations.

With our membership at RE100, we are dedicated to make our contribution to global decarbonization.

(1) absolute reduction, base year 2021, (2) Net zero: GHG emissions released into the atmosphere equal the amount removed (3) based on RE100 criteria

SUSTAINABILITY HIGHLIGHTS



completed in 2024

GERMANY:

Power Purchase Agreement starting from 2024, with annual volume of 60 GWh

SINGAPORE:

RBA Platinum Award for results of sustainability audit in 2024



THE SILTRONIC EXECUTIVE BOARD



Dr. Michael Heckmeier CEO

- CEO since May 2023
- 25 years at Merck
- Studied physics and mathematics, PHD in physics
- MBA, general management



Claudia Schmitt CFO

- CFO since July 2023
- 15 years at Siltronic
- 11 years at Wacker Chemie AG
- Studied business administration



Klaus Buchwald COO

- COO since June 2024
- 21 years at Infineon
- 4 years at Rohde & Schwarz
- Studied mechanical and industrial engineering



EXECUTIVE SUMMARY

KEY TAKEAWAYS – CLEAR COMMITMENT TO PROFITABLE GROWTH AS A TECHNOLOGY LEADER



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Financial Calendar and Events

Sep 2, 2025 **ODDO BHF Corporate** Conference, Frankfurt Conference,

Sep 3, 2025 DB TMT

London

Sep 22, 2025 **Berenberg and GS German Corporate** Conference, Munich Sep 23, 2025

Baader Investment Conference, Munich results

Oct 28, 2025 **Q3**

MKK, Munich

Nov 12, 2025

Nov 13, 2025 **MS TMT** Conference, **Barcelona**

Investor Relations Contacts







Stephanie Malgara investor.relations@siltronic.com Tel. +49 89 8564-3133

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