



# **EQUIPMENT ENGINEER (ENGINEERING / FINISHING)**

## **RESPONSIBILITIES**

- Maintenance and troubleshooting of Semiconductor equipment
- Monitor equipment indices and sustain equipment uptime performance by conducting failure pareto analysis
- Coordinate with Process engineers to resolve process related issues
- ➔ Forecast and review equipment spare parts
- Plan and implement effective preventive and predictive maintenance
- → Review Job Hazard Analysis (JHA) and Preventive Maintenance (PM) task list periodically

### **REQUIREMENTS**

- → Bachelor of Electrical/ Electronics/ Mechanical/ Mechatronic Engineering
- ➔ Preferably 2 years of Semiconductor wafer fab process equipment experience
- Knowledge of computer-based control system, electro-mechanical pneumatics applications, vacuum and plasma technologies; high thermal wafer processing
- → A dynamic team player

### SILTRONIC AG

Siltronic is one of the world's largest manufacturers of hyperpure silicon wafers and partner of many leading semiconductor companies. The company employs around 3,800 people. The company has a network of state-of-the-art production sites in Asia, Europe and the USA. Siltronic develops and manufactures silicon wafers in diameters of up to 300mm. These are the basis for modern microelectronics and nanoelectronics.

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