



DR. IR. JOS P.H. BENSCHOP

Corporate Vice President Technology der ASML Netherlands BV, Niederlande

Date of Birth: 31 Mai 1960 Nationality: Dutch

Member of the Supervisory Board since 5 May 2023

EDUCATION

1984	MSc (cum laude) from Twente University of Technology, physics faculty, Netherlands
1989	PhD from Twente University of Technology, physics faculty, Netherlands

PROFESSIONAL CAREER

1984 – 1995	Scientist at Philips Nat.Lab, Netherlands and Philips Research Lab Sunnyvale, USA
1995 – 1996	Innovation Manager CD-R at Philips Consumer Electronics
1997 – 2011	Vice President Research at ASML, Netherlands
2011 – 2021	Professor for Industrial Physics at Twente University of Technology, Netherlands (part-time)
2011 – 2023	Senior Vice President Technology at ASML, Netherlands
Seit 2023	Corporate Vice President Technology at ASML, Netherlands

FURTHER MEMBERSHIPS IN LEGALLY REQUIRED SUPERVISORY BOARDS IN GERMANY AND COMPARABLE SUPERVISORY BODIES OF GERMAN AND FOREIGN COMPANIES

- Member of the Supervisory Board of Cymer Light Source Technology
- Chair of international advisory council NanoLabNL

ADDITIONAL ACTIVITIES

- Appointed by the King of The Netherlands as member of Advisory Council Science, Technology and Innovation "AWTI"
- Member of Netherlands Academy of Technology and Innovation AcTI

PERSONAL OR BUSINESS REALTIONSHIPS PURSUANT TO RECOMMENDATION C.13 OF THE GERMAN CORPORATE GOVERNANCE CODEX

None



RELEVANT KNOWLEDGE, SKILLS, AND EXPERIENCE

As Corporate Vice President of a leading international manufacturer of high-tech semiconductor equipment, Dr. Benschop has extensive and profound know-how in the markets relevant to the Siltronic Group. Dr. Benschop also has extensive experi-ence in the strategic development and implementation of semiconductor technolo-gies. In his management function, Dr. Benschop has qualified experience in the strategic direction and development of a company in general. As an advisor to the Dutch government and the Parliament for Science, Technology and Innovation, Dr. Benschop is well versed in R&D and has successfully led R&D projects in the semi-conductor field during his career.